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H. Peter Hofstee is a Distinguished Research Staff Member at IBM Infrastructure, Austin, TX USA and professor, Big Data Systems (part time) at TU Delft, Netherlands. Dr. Hofstee is best known for his contributions to high-performance processors and systems, ranging from the first 1GHz processor published in 1998 to the Cell Broadband Engine Processors that were used in systems ranging from the Playstation 3 to the world's first supercomputer to reach 1 Petaflop sustained operation as well as in a host of embedded and signal processing applications. As the first heterogeneous shared memory processor the Cell Broadband Engine architecture set new standards in performance and energy efficiency. Dr. Hofstee's Big Data Systems contributions include reconfigurable accelerators for big data, compression technology that is deployed in IBM mainframe, power, and storage products, and large-scale sorting and bioinformatics applications. His current work focuses on infrastructure ranging from embedded cloud-connected devices to the cloud infrastructure that aggregate and analyze their data leveraging Al. Dr. Hofstee has published widely and holds more than 150 patents on topics ranging from circuit design and technology, heterogeneous computer architecture, to security, data compression, and other system-related topics.



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