



## CONFERENCE SCHEDULE

### DAY 1 - Friday (12.02.2021)

09.00AM – 09.25 AM	Participants / Attendees Joining the Meeting Hall		
09.30 AM –10.30 AM	Conference Inauguration		
10.45 AM – 11.25 AM	<b>Keynote 1: Prof. Hidenori Mimura, Director, Research Institute of Electronics, Shizuoka University Japan,Hirosaki</b> Session In-charge: <b>Dr.Ramakrishnan V.N</b>		
11.30 AM – 12.10 PM	<b>Keynote 2: Dr. Kaushik Roy, : Professor, Electrical and Computer Engineering, Purdue University</b> Session In-charge: <b>Dr.Sivasankaran K</b>		
12.15 PM – 12.55PM	<b>Keynote 3: Mr.Shivraj Gurpadappa Dharne, India Site Lead, Technology Enablement, GLOBALFOUNDRIES</b> Session In-charge: <b>Dr. Abdul Majeed K K</b>		
12.55 PM – 2.00 PM	LUNCH		
2.00 PM – 2.40 PM	<b>Keynote 4: Dr. Jay Shah, India Site Lead, Fab Engineering, GLOBALFOUNDRIES</b> Session In-charge: <b>Dr.Rajan Pandey</b>		
3.00PM - 3.40PM	<b>Keynote 5 : Dr. Stuart Parkin, Max Planck Institute of Microstructure Physics in Halle</b> Session In-charge: <b>Dr. Rajan Pandey</b>		
4.00 PM – 6.00 PM	<b>Technical presentation Session – 1</b> <b>Digital Design for Signal, Image and Video processing</b> <b>Session Chair: Dr.Sridevi S</b>	<b>Technical presentation Session – 2</b> <b>VLSI Testing and Verification</b> <b>Session Chair: Dr.Sivanantham S</b>	<b>Technical presentation Session – 3</b> <b>Emerging Technologies &amp; IoT</b> <b>Session Chair: Dr.Sakthivel R</b>
4.30PM -5.15 PM	<b>Keynote 6: Dr.Saifur Rehman, Professor,ECE, Virginia Tech, USA</b> Session In Charge: <b>Dr.Jacob Raglend, Professor, SELECT</b>		

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## CONFERENCE SCHEDULE

### DAY 2 - SATURDAY (13.02.21)

9.30 AM – 10.10 AM	<p><b>Keynote 7:</b> Dr. Daniel Moraru, Associate professor, Department of Electronics and Materials Science, Shizuoka University, Japan. Session In-charge: <b>Dr. Ramakrishnan V.N</b></p> <p><b>Keynote 8:</b> Dr. Prabha Sundara vadivel, Assistant Professor, Electrical Engineering, The University of Texas at Tyler Session In-charge: <b>Dr. Jagannadha Naidu K</b></p>		
10.10 AM – 10.50 AM	<p><b>Keynote 9:</b> Mr. VINAYAK BHARAT NAIK, MRAM Device Lead, GLOBALFOUNDRIES, SINGAPORE Session In-charge: <b>Dr. Arun Dev Dhar Dwivedi</b></p> <p><b>Keynote 10:</b> Dr. Amey Kulkarni, Deep Learning Engineer at NVIDIA, San Jose, California, United States. Session In-charge: <b>Dr. Hariharan I</b></p>		
10.50 AM – 11.30 AM	<p><b>Keynote 11:</b> Dr. David Rogers Founder/ Director, Nanovation, France Session In-charge: <b>Dr. Pradheep T</b></p>		
11.30 AM – 11.45 AM	Break		
11.30 AM – 1.30 PM	<p><b>Technical presentation Session – 4</b> <b>Nano-scale Modelling and process Technology Device</b> <b>Session Chair: Dr. Ramakrishnan V N</b></p>	<p><b>Technical presentation Session – 5</b> <b>Analog and Mixed Signal Design</b> <b>Session Chair: Dr. Kumaravel S</b></p>	<p><b>Technical presentation Session - 6</b> <b>Communication Technologies and Circuits</b> <b>Session Chair: Dr. Vikas Vijayvargiya</b></p>
1.30 PM – 2.00 PM	LUNCH		
2.00 PM – 2.45 PM	<p><b>Keynote 12 :</b> Mr. Sripad Sheshagiri Nagarad, India Site Lead, Pre &amp; Post Fab Operations GLOBALFOUNDRIES, Bangalore Session In-charge: <b>Dr. Jayarishnan P</b></p>		
2.45 PM – 3.30 PM	<p><b>Keynote 13` :</b> Dr. Arindam Sanyal, Department of electrical engineering, University of Buffalo. Session In-charge: <b>Dr. Kumaravel S</b></p>		
3.30 PM - 3.45 PM	Break		
3.30 PM - 5.30 PM	<p><b>Technical presentation Session – 7</b> <b>Technology and Modelling for Micro Electronic Devices</b> <b>Session Chair: Dr. Rajan Pandey</b></p>	<p><b>Technical presentation Session – 8</b> <b>Electronics for Green Technology</b> <b>Session Chair: Dr. Jagannadha Naidu K</b></p>	

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