

## 3<sup>rd</sup> International Conference on Microelectronic Device Circuits and Systems (ICMDCS 2022)

### PRE-CONFERENCE (VIRTUAL) TUTORIAL SCHEDULE (11.08.2022)

	<i>Track-1</i>	<i>Track-2</i>
09.15 AM – 09.25 AM	<i>Joining the Meeting Hall (Online)</i>	<i>Joining the Meeting Hall (Online)</i>
09.30 AM – 11.00 AM	<b>“Machine Learning meets IoT”</b> by Dr. Bharath Sudharsan , Senior AI-ML Software Engineer, General Motors. <i>Session Incharges: Dr.P. Jayakrishnan &amp; Dr Dhanabal R</i>	<b>“MOSFET compact modeling”</b> by Dr Vaidyanathan Subramanian, Deputy Director, Design Enablement (SPICE models), GLOBALFOUNDRIES <i>Session Incharges: Dr. Jagannadha Naidu &amp; Dr.Rajeev Pankaj</i>
11.00 AM – 11.30 AM	Break	
11.30 AM – 1.00 PM	<b>“Machine Learning meets IoT”</b> by Dr. Bharath Sudarshan , Senior AI-ML Software Engineer, General Motors. <i>Session Incharges: Dr.P. Jayakrishnan &amp; Dr Dhanabal R</i>	<b>“MOSFET compact modeling”</b> by Dr Vaidyanathan Subramanian, Deputy Director, Design Enablement (SPICE models), GLOBALFOUNDRIES <i>Session Incharges: Dr. Jagannadha Naidu &amp; Dr.Rajeev Pankaj</i>
01.00 PM – 2.00 PM	LUNCH	
2.00 PM – 3.30 PM	<b>“Intel FPGAs for AI and Machine Learning”</b> , by Mr. Padmanaban K, Software Enabling and Optimization Engineer, Intel PSG <i>Session Incharges: Dr.P. Jayakrishnan &amp; Dr Dhanabal R</i>	<b>“Basic of Design for Test &amp; Diagnosis”</b> by Mr.Jaidev Shenoy, Marvell & Mr.Atul Chhabra, Mr.Bharath Nandakumar, Cadence <i>Session Incharges: Dr. Jagannadha Naidu &amp; Dr.Rajeev Pankaj</i>
3.30 PM – 4.00 PM	Break	
4.00 PM – 5.30 PM	<b>“Intel FPGAs for AI and Machine Learning”</b> , by Mr. Padmanaban K, Software Enabling and Optimization Engineer, Intel PSG <i>Session Incharges: Dr.P. Jayakrishnan &amp; Dr Dhanabal R</i>	<b>“Basic of Design for Test &amp; Diagnosis”</b> by Mr.Jaidev Shenoy, Marvell & Mr.Atul Chhabra, Mr.Bharath Nandakumar, Cadence <i>Session Incharges: Dr. Jagannadha Naidu &amp; Dr.Rajeev Pankaj</i>