



One day wEBINAR

on

**OppOrtunities and chal lenges in c0nverting ideas int0 pr0ducts
29th aUGUST 2020**

Organized by TIFAC-CORE in Automotive Infotronics jointly with School of
Information Technology and Engineering, VIT Vellore

Today the younger generation is highly motivated to do projects in their core domains. A large number of real-time problems are addressed at the student level, and this student project needs proper support and guidance to convert into a product. This webinar focuses on supporting the students and faculty members interested in turning their ideas, projects into product development.

Focus Area

- ✓ Idea Generation in Innovation Framework
- ✓ Design Thinking in Innovation Framework
- ✓ A primer for Innovation and Entrepreneurship
- ✓ Converting Student Project Ideas into Prototypes

Important Dates

Last Date of Registration : 27.08.2020

Confirmation Mail with Link : 28.08.2020

Conference Time : 10 AM TO 1 PM on 29th August 2020

Lecture Delivery Mode : Microsoft Teams

No Registration Fee!

E-Certificate of Participation will be given to the attendees if they attend the whole program.

[Speaker Profile](#)

Session – 1	<p>A Primer for Innovation and Entrepreneurship</p> <p>Dr. Balachandar. A, GM, TBI, VIT</p> <p>He is a certified mentor in the National Entrepreneurship Network (NEN), certified infoDev (The World Bank Group) Business Incubation Trainer and a certified Enterprise Development and Investment Promotion (EDIP) trainer under a UNIDO-CII-DST program. He is passionate about technology startups, grooming students and assisting innovators.</p> <p>He has around thirty years of 'Techno-commercial' experience and is in-charge of VIT-TBI from its inception in the year 2003. He mentored and assisted a number of startups and many innovators during the last 17 years. He has been coordinating a number of innovation and entrepreneurship programs (training, funding and capacity development) with the support of various departments of Govt.of India. He successfully executed a project for incubation capacity building initiatives under World Banks' infoDev program during 2006-07. He coordinated a 'Youth Entrepreneurship Skills Development Initiative' project supported by UNIDO for building entrepreneurship and incubation capacity building targeting four West African countries during 2012. He has raised a grant from Biotechnology Research Assistance Council (BIRAC) to set up a BioNEST Bioincubation program @ VIT-TBI during 2017. He is coordinating the NIDHI PRAYAS Scheme of DST at VITTTBI and MeitY supported TIDE 2.0 incubation program.</p>
Session – 2	<p>Converting Student Project Ideas into Prototypes</p> <p>Dr. K. Ganesan , Director, TIFAC-CORE in Automotive Infotronics, VIT Vellore</p> <p>Dr. K. Ganesan is working at Vellore Institute of Technology, Vellore for the past 18 years. He is currently working as the Senior Professor, School of Information Technology and Engineering and Director of TIFAC-CORE in Automotive Infotronics, Vellore. He has published nearly 100+ papers in journals and conferences. He has completed more than 25 consultancy projects. He has filed 26 patents including a US patent granted. He insists students to work on innovative projects and file patents which in turn helps them in getting job offers in R & D units of core industries. He works on emerging areas such as AI, ML, Security (Cyber), Big Data, IoT and decision support systems.</p>

Session – 3	<p>Idea Generation in Innovation Framework</p> <p>Mr. M R Nagaraj</p> <ul style="list-style-type: none"> • Working as technology head at fromideas.com and leading innovation team for product development at www.innovationcenter.in • Sr. Consultant with 22 years of experience in handling software product, project execution. • Rich experience in project management and activities with insightful knowledge of planning, analysis, design, development, testing, configuration management and training • Significant experience of working with Clients, Delivery Managers, Project Managers, Enterprise Application (EAI) and Project Management, Resource Management and Technical Teams for securing & executing multiple projects
Session – 4	<p>Design Thinking in Innovation Framework</p> <p>Mr. Padmanabh H</p> <ul style="list-style-type: none"> • Professional with over 25 years of successful propelling ERP and GRC Implementations (Over a decade SAP Security/ GRC Access Control, Process Control & Risk Management projects & 10+ ITGRC Compliance & Audit. • SME & Professional Trainer: vCISO, Innovation Center, MetricStream GRC, SAP Security & GRC, InfoSec & SOx for over 4000 hrs for MNCs such as Sony, IBM, Accenture, JK Organizations, Cognizant, Wipro, Deloitte R10 AERS. • Speaker on GRC, vCISO at various Institutions / Org's including VIT on InfoSec, ISACA, in 19th Karnataka Annual Conference on "Audit for Compliance", ISACA "New Technology Identification Working Group" Active Contributor.

About VIT

VIT, one of the premier institutes in Tamil Nadu, was established in 1984. It is a major, comprehensive, student-centered research university dedicated to excellence in teaching, research, and service. VIT today comprises of various schools and interdisciplinary research centres offering undergraduate postgraduate and research programs in various disciplines. The institute was established to provide quality higher education at par with international standards. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Presently the student's strength has crossed 25,000. Our Memoranda of Understanding with various international universities are our major strength. The mission of VIT is to educate students from all over India, including those from the local and rural areas, and from other countries, so that they can become enlightened individuals, improving the living standards of their families, industries, and society. VIT provides individual attention, world-class quality education, and takes care of character building. There are student and faculty exchange programs, to encourage joint research projects for the mutual benefit. The institute was recently ranked No.1 Private Engineering Institution by MHRD, Govt. of India. VIT – Recognized as Institution of Eminence (IoE) by the Government of India.

About TIFAC CORE

TIFAC CORE in Automotive Infotronics is an initiative of Government of India wherein the collaboration between Industry and Academia is insisted. Keeping this in mind the Centre is offering a PG program on "Automotive Electronics". The course is offered in collaboration with School of Electronics Engineering (SENSE) at Vellore campus. The students are exposed to various emerging technologies and hence are getting jobs in R & D Centres. The centre is also conducting a number of short term training programs (workshops) in order to inculcate inter-disciplinary real time problem solving approach among students. The centre has completed 25 consultancy projects (with Industries) and filed more than 25 patents.

About School of Information Technology and Engineering

The School offers programmes related to Information Technology, Software Engineering, Computer Applications and Domain Specific Applications. It imparts the knowledge and skills required for the students to become competent and excel in the broader domain of Information Technology and allied disciplines. The students are encouraged to work on challenging projects and given exposure to industry - Oriented practices. The School focus is on holistic learning to help the students to make significant contributions to the Information Technology industry and to serve the society at large. The school has more than 4,800 students and 180 committed faculty members, apart from many visiting professors, working professionals from the industry and R&D organizations. The faculty members are highly motivated to do ground-breaking research and excel in teaching and learning processes. They provide opportunities for the students to apply the acquired knowledge for solving real-world problems and gaining undergraduate research experience. The placement record of the school is always impressive.

Organizing Committee

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Mr. Sankar Viswanathan, Vice President
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ORGANIZING CHAIR

Dr. K. Ganesan, Director, TIFAC-CORE in Automotive Infotronics,VIT Vellore

Program Coordinator

Dr.R.K.Nadesh, Associate Professor, School of Information Technology and Engineering, VIT Vellore

Dr.R. Srinivasa Permaal, Associate Professor, School of Information Technology and Engineering, VIT Vellore

Any Queries, Please contact

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