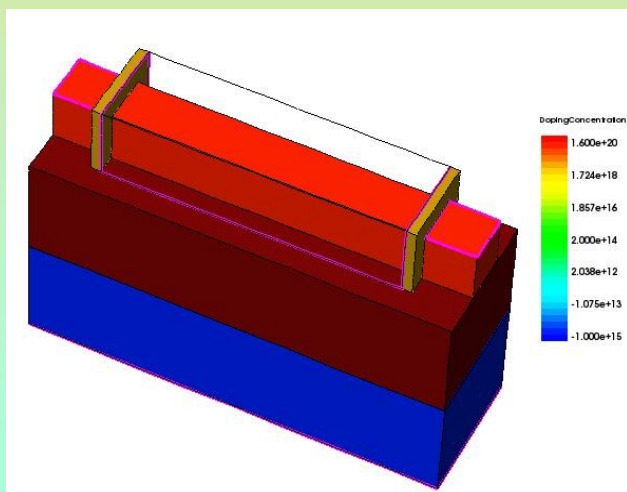
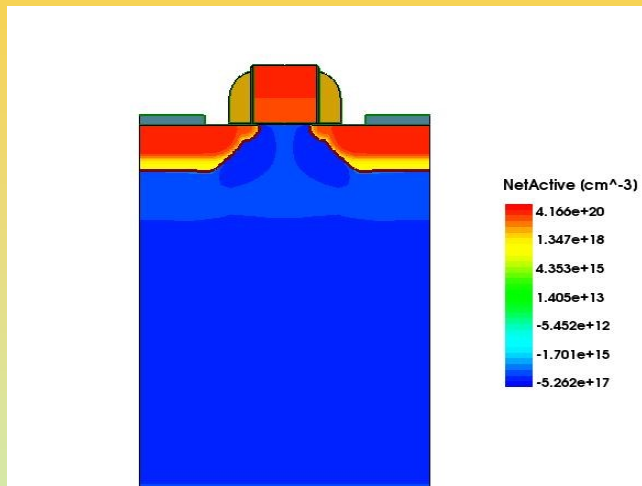


**Three Days Hands-on Training
Program on
Technology CAD (TCAD)**

21st - 23rd April, 2023



Organized by

**Department of Micro & Nanoelectronics,
School of Electronics Engineering,
VIT, Vellore-632014**



VIT[®]
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

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School of Electronics Engineering,
VIT, Vellore.



**Three Days Hands-on Training
Program on
Technology CAD (TCAD)**

(Capital Letters only)

Full Name

Reg.No :

Specialization :

Organization & Address:

Mobile No.:

E-Mail:

Declaration: The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the course and attend the course for the entire duration.

Place:

Date:

Signature of the Participant

**Venue: 237A, TT Building, 2nd Floor,
VIT, Vellore-632014.**

Time : 09:30 AM - 05:30 PM

21st - 23rd April, 2023

Important Date:

Last date for registration: 15th April, 2023

About the Institute

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spur us on in our relentless pursuit of excellence. In fact, it has become a way of life for us. The highly motivated youngsters on the campus are a constant source of pride.

Our Memoranda of Understanding with various international universities are our major strength. They provide for an exchange of students and faculty and encourage joint research projects for the mutual benefit of these universities. Many of our students, who pursue their research projects in foreign universities, bring high quality to their work and esteem to India and have done us proud. With steady steps, we continue our march forward.

About School of Electronics Engineering

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. B.Tech. Electronics and Communication Engineering is accredited by the Engineering Accreditation Commission of ABET, Students who are eligible are placed on campus and many of them are placed in core companies every year. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. The latest simulation tools are used to cater to various

specializations and are equipped with facilities for measurement, characterization and synthesis of experimental as well as theoretical results. Students are encouraged to take up their final year projects abroad too. The School has many industry sponsored advanced laboratories for carrying research and development. MoUs with many Foreign Universities, Research Organizations and Industries facilitate student and faculty exchange. Faculty are actively involved in R&D activities and are working on research projects funded by government organisations like DRDO, ISRO (RESPOND), BRNS and agencies like DST.

Target Audience

Students, research scholars & Faculty members from Engineering colleges.

About The Workshop

The relentless march fast of the CMOS has slowed down and the semiconductor industry is looking for novel and innovative devices. Many novel devices are being explored currently. TCAD tool allows us to generate new structures, circuits and analyze its performance. Unlike circuit simulators, TCAD needs a special training. This hands-on training addresses this gap.

Schedule of the this three days hands-on training is as follows:

Day 1 (21st April 2023 - Friday)

- Session 1 - Introduction to TCAD
- Session 2 - PN Junction 2D/3D Device generation
- Session 3 - Device simulation & Characteristics
- Session 4 - Hands-on training session

Day 2 (22nd April 2023 - Saturday)

- Session 1 - MOSFET Device generation
- Session 2 - Device simulation & Characteristics
- Session 3 - Hands-on training session (2D type)
- Session 4 - Hands-on training session (3D type)

Day 3 (23rd April 2023 - Sunday)

- Session 1 - FinFET Device generation
- Session 2 - Device simulation & Characteristics
- Session 3 - Hands-on training session
- Session 4 - Advanced Device generation

Resource Persons & Coordinator

**Dr. V.N. Ramakrishnan &
Dr. V. Nithish Kumar,
Dr. T. Pradheep**

Associate Professor,
Dept. of Micro & Nanoelectronics,
School of Electronics Engineering,
VIT, Vellore, TN-632014
vnramakrishnan@vit.ac.in, nithishkumarv@vit.ac.in

Registration Fees:

Students & Scholars : Rs. 2000/-*

**Faculty &
Industry participants : Rs. 3000/-***
(* - Inclusive of GST)

Registration will be based on First come first serve basis. The number of participants is limited to 60. Lunch and snacks will be provided.

Registration Link: <https://events.vit.ac.in/>