

About the Institute

VIT was founded in 1984 as Vellore Engineering College by the Chancellor, Dr. G. Viswanathan. From its humble beginnings, the institution has grown exponentially to that of more than 35,000 students. It was conferred the University status in 2001 in recognition of its excellence in academics, research and extracurricular initiatives. Currently, VIT has 4 campuses – in Vellore, Chennai, Amaravati (AP) and Bhopal (MP). VIT has been consistently ranked among the best institutions of the country, and is aspiring to emerge as a global leader. The National Institutional Ranking Framework (NIRF) of the MHRD, Government of India, has identified VIT as the best Private Engineering Institution in India.

With students from all the states of India and from more than 50 countries, the cosmopolitan VIT provides an appropriate ambience for holistic learning and comfortable living. Sports, games and cultural activities are an integral part of student life on campus. VIT holds an exemplary placement record by consistently placing more than ninety percentage of the students in good companies. The VIT's international linkages provide ample opportunities for students and faculty to gain global exposure. VIT alumni, spread across the world, are serving the most advanced as well as the most deprived societies.

About School of Electrical Engineering

School of Electrical Engineering (SELECT) has 95 faculty members who have done their UG and PG degrees from the top-notch universities. The School offers B.Tech (Electrical and Electronics Engineering),

B.Tech (Electronics and Instrumentation Engineering), M.Tech (Power Electronics and Drives) and M. Tech (Control and Automation), M.S. by Research and Ph.D. in Engineering. B.Tech (Electrical and Electronics Engineering) and B.Tech (Electronics and Instrumentation Engineering), is accredited by the Engineering Accreditation Commission of ABET. All UG & PG programmes of the school are accredited by the Institution of Engineering and Technology (IET), U.K. The placement record of the School has always been impressive. Almost 100% of the students get job from the campus placement and many of them are getting it in core companies every year. The School has state-of-the art laboratories in almost all the areas of Electrical, Electronics and Instrumentation Engineering. Every year, students get scholarships to do their final year projects abroad under the Semester Abroad Program (SAP). Schneider Electric, India and NxP Semiconductors, India, have established Centre of Excellence for students R&D activities under the guidance of faculty members and Industry experts. The School has signed MoUs with many foreign Universities, research organizations and Industries from where students get benefits for their R&D Work / Projects from the MoUs.

About VAP

The main objective of this programme is to provide the participants an in depth understanding and hands-on training related to (i) solving power system problems using Matlab and MI power software tools. (ii) Electrical machines both AC and DC and (ii) concepts of protective relays and circuit breakers and coordination between them. This program includes the practical exposure of the AC and DC machines and various protective schemes. Since the VAP

aims at equipping the participants with a wide scope of tools the programme spans for a period of 30 hours on 09th, 11th, 14th and 21st of April 2024. The outcome of the VAP aims at providing comprehensive overview and understanding of the machines, software used to solve power system problems and relays and circuit breakers.

Topics to be covered

*Basics of Electromechanical Energy conversion devices with practical session for deep understanding of electrical machines.

*Analysis of Power system problems using Mat lab, MI power software etc.

*Understanding the basics of conventional protection scheme in-coordination with circuit breaker with both theoretical concepts and practical sessions.

Registration Fee:

Students and Research Scholars: **100/-**

(Excluding GST)

(Certificate will be issued to all the registered participants)

Prospective participants are requested to register for the program through the following link.

<https://events.vit.ac.in/>

Maximum 45 participants will be accommodated in the VAP. Hence preference will be given on first come first serve basis.

Thirty Hours Value Added Program
On
"Electrical Machines and Power System
Protection"

REGISTRATION FORM

(Capital Letters only)

Full Name

Gender: Male / Female.

Reg.No:.....

Mobile No.:

E-mail :

Receipt No:

Amount : .. Date :

Declaration: The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the course and attend the course for the entire duration.

Place :

Date

Signature of Participant

ADVISORY COMMITTEE

Advisors

Dr. Mathew Mithra Noel

*Professor & Dean,
SELECT, VIT, Vellore.*

Dr. K. Sathish Kumar

*Professor & Head,
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Technical Support Staff

Selvaganesan V M and Srinivasan U

Technical Officer, SELECT, VIT Vellore



VIT[®]

Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

School of Electrical Engineering

Thirty Hours Value Added Program
On

**"Electrical Machines and Power
System Protection with hands-on
training"**

Course Dates:

**09/04/2024, 11/04/2024, 14/04/2024 and
21/04/2024**



Coordinators

**Dr. K. Sathish Kumar
Dr. S. Prabhakar Karthikeyan**

Organized by

**School of Electrical Engineering,
Vellore Institute of Technology, Vellore**