### **About SENSE:**

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

#### About the event:

The objective of the training program is to provide recent advancements and research outcomes pertaining to Memristor and its applications in Neuromorphic circuits. The FDP includes sessions focused on technologies, techniques and applications with intent to foster the exchange of knowledge and ideas between experts. It will be a good platform for those who started their research in the field of VLSI, semiconductor devices, neuromorphic devices, bio-inspired electronics.

#### **Course Content:**

- Neuromorphic Synaptic Transistors
- Memrister Bridge Synapse
- Application of Memrister in Neuromorphic Circuits
- Xilinx IP Core Applications
- Applications of Organic Semiconductors
- Advanced MOSFET structures
- Bio-inspired Electronics

#### **Resource Persons:**

Dr. Sankar Prasad Bag, Dongguk University, South Korea Dr. Avatar Singh, Adama Science and Technology University, Ethiopia Dr. Ram Kaji Budhathoki, Kathmandu University, Nepal Dr. Zubaer Ibna Mannan, Kyungdong University Global Campus (Gosung), South Korea

Dr. B. Khaleelu Rehman, Chaitanya Bharathi Institute of Technology, Hyderabad

Dr. Upendra Kumar Verma, Dev Bhoomi University, Dehradun

## **Payment Link:**

Internal Participants (Vellore) : <u>https://tinyurl.com/yujsumnv</u> External Participants: <u>https://events.vit.ac.in/</u>

## **Advisory Committee:**

Dr. Sivanantham S

Professor and Dean,

School of Electronics Engineering (SENSE),

Vellore Institute of Technology, Vellore, India.

Dr. Jagannadha Naidu K

Assistant Professor & Head, Department of Micro & Nanoelectronics, School of Electronics Engineering (SENSE),

Vellore Institute of Technology, Vellore, India.

Dr. Noor Mohammed V

Professor & Head,

Department of Communication Engineering,

School of Electronics Engineering (SENSE), Vellore Institute of Technology, Vellore, India.

### **Coordinators**

Dr. Vetriveeran Rajamani

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**Eligibility:** The training program is open to Industry personnel, Engineering Faculties, Research Scholars and UG/PG students.

#### **Registration Fee :**

Rs.400 +18% GST (For Faculty/Industry Persons), Rs.200 +18% GST (For Students and Research scholars). Last Date for Registration:

**December 5, 2023** 



## Three Day Faculty Development Program (FDP)

on

Recent Trends and Research Challenges of VLSI in Memristor Based Neuromorphic Circuits (Online mode)

## 7<sup>th</sup> – 9<sup>th</sup> December 2023

Organized by

Department of Micro & Nano Electronics and CommunicationEngineering School of Electronics Engineering (SENSE) Vellore Institute of Technology Vellore-632014

# **School of Electronics Engineering (SENSE)**

3 - Day FDP on "Recent Trends and Research Challenges of VLSI in Memristor Based Neuromorphic Circuits"

Events Schedule		
Date	Session	Session
	10.00 AM to 12.00 PM	2.00 PM to 4.00 PM
	Dr Sankar Prasad Bag Dongguk University South Korea	Dr Ram Kaji Budhathoki Kathmandu University Nepal
07-12-2023	Topic: Exploring Hydrogel-Based Neuromorphic	Topic: Memristor Bridge Synapse Based
	Synaptic Transistors: A Revolution in Bio-	Learning in Multilayer Neural Networks
	Inspired Electronics	
08-12-2023	Dr B. Khaleelu Rehman Chaitanya Bhartathi Institute of Technology Hyderabad, India. Topic: Xilinx IP Cores Application	Dr Zubaer Ibna Mannan Kyungdong University Global Campus (Gosung) South Korea. Topic: Memristor and its Applications in Neuromorphic Circuits.
09-12-2023	Dr Upendra Kumar Verma Dev Bhoomi University Dehradun, India Topic: Novel Applications of Organic	Dr Avatar Singh Adama Science and Technology University Ethiopia Topic: Challenges and Research Opportunities in
	Semiconductors in Solid State Devices	VLSI Devices