VAP objective:

This VAP course is dedicated in training the students on the design and manufacturing of a Printed Circuit Boards for wearable device development and other electronic applications. This workshop is an invaluable resource for those who are learning PCB design. Designing a PCB is not something you will do in a couple of hours. It is a highly technically learned skill that will take years to master. The Workshop will provide the basics of PCB design as well as more advanced topics. This workshop focuses on advanced topic of PCB design along with fabrication process. Each participant will involve in circuit schematic construction, netlist creation, track designing, gerber file generation, easyCAD, CAMAuto, PCB printing and finally assembling and soldering the electrical components onto the board.

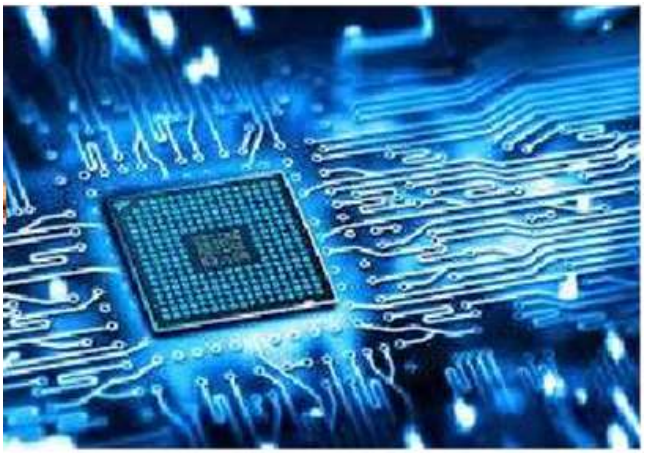
About the VAP:

A printed circuit board (PCB) is used to mechanically support and electrically connect electronic components using conductive pathways, tracks or signal traces printed by electro-mechanical method using Auto-lab workstation on copper conductive sheets. In the recent past, the printed circuit boards are used in all the commercially produced electronic devices, and allow fully automated assembly processes that were not possible or practical in earlier era of circuit assembly processes. Consumers are driving industry growth with a desire for cool products. Great technology is no longer enough to differentiate a product. It must be squeezed into a "cool" package in order to sell. This has an effect on everything inside the box --including the PCB, the chips and what's packaged on the chips.

Value Addition Programme (VAC1401)

On

Design and Fabrication of Printed Circuit Boards using Auto-lab



21st Sep, 5th Oct, 12th Oct,
9th Nov & 16th Nov-2024
Timings: (10.00 AM – 5.00 PM)

TT312 & TT502

Resource Persons:

1. Mr. Harish S – Valeo India Pvt Ltd.
2. Mr. Gautam P A – Delta Electronics India Ltd.
3. Dr. Elizabeth Rufus, Professor-HAG, SENSE, VIT
4. Dr. Kathirvelan J, Professor Grade1, SENSE, VIT

Organized by:

Department of Sensor and Biomedical Technology

School of Electronics Engineering

Vellore Institute of Technology

Vellore-632014

VIT
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

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Registration Form

Name:.....

Reg. No:.....

Programme:.....

Branch:.....

Affiliation:.....

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.....

Address:.....

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Mobile No:.....

E-mail id:

.....

Online Transaction No:

.....

Signature of the Participant

Registration Fee:

Rs.500/- (Including 18% GST)

(Seats will be allotted based on a first come first serve basis)

Who can attend?

- UG, PG and Ph.D Scholars of all disciplines.

Requirements:

- Participants need to come with their Laptops for the course.

Coordinators:

Dr. Elizabeth Rufus,

Professor-HAG,

School of Electronics Engineering,

Vellore Institute of Technology,

Vellore 632 014

Tamilnadu, India

Dr. Kathirvelan J,

Professor Grade1,

School of Electronics Engineering,

Vellore Institute of Technology,

Vellore 632 014

Tamilnadu, India.

E-mail: j.kathirvelan@vit.ac.in

Mobile: 9500356597

To register for the programme, kindly send the scanned copy of the duly filled registration form along with online transaction number through e-mail to j.kathirvelan@vit.ac.in on or before 15th Sep. 2024. Registration is open until seats are filled. Payment should be made through only online using the link given below.

Registration Link: https://events.vit.ac.in/

(E-certificates will be issued to the participants after attending all the sessions of the VAP course)