

VDAT 2024 Conference Schedule (02/09/2024-03/09/2024)

Conference Day -1 (02/09/2024)					
8 AM to 9.30 AM	Registration				
9.30 AM to 11 AM	Inauguration				
	Venue: Anna Auditorium				
11 AM to 11.30 AM	High Tea				
	Venue: CS Hall				
11.30AM to 12.15PM	Keynote-1 (Mr.Navin Bishnoi, Country Head - Marvell)				
	Venue: Anna Auditorium				
12.15 PM- 1.00 PM	Keynote-2 (Siemens)				
	Venue: Anna Auditorium				
1 PM to 2 PM	Networking Lunch				
	Venue: Foodys				
2 PM to 2.30 PM	Keynote--3 (Ms. Shamitha Rao - Sr. Manager, Solutions Group, Synopsys)				
	Venue: Anna Auditorium				
2.30 PM - 3.00 PM	Keynote—4 (Mr. Alok Kuchlous, CEO, Mirafra Technology)				
	Venue: Anna Auditorium				
3.00 PM to 4 PM	Poster Presentation, PhD Research Forum and Networking Break Venue: CS Hall				
4 PM to 05:30 PM	Track 1 (Emerging Devices and Material Technologies)	Track 1 (Testing and Verification)	Track 1 (VLSI Circuits and System Design)	Track 1 (CAD for VLSI)	Track 1 (Emerging Processors for System Design)
	Venue: TT312		TT727		
05:45 PM – 06:15 PM	Exploring Career Opportunities in Semiconductor Industry by leading Experts from Govt.organization/Industry/Academia Venue: Anna Auditorium				
6:15 PM to 7:15 PM	Cultural Programme Venue: Anna Auditorium				
7:15PM Onwards	Networking Dinner Venue: Foodys				

Conference Day-2 (03/09/2024)					
8 AM to 9.30 AM	Registration				
	Venue: CS Hall				
9.30 AM to 10.00 AM	Keynote—5 (Dr. Poki Chen, National Taiwan University of Science and Technology, Taiwan)				
	Venue: Anna Auditorium				
10.00 AM to 10.30 AM	Keynote--6 (TBD)				
	Venue: Anna Auditorium				
10:30 AM to 11:00 AM	Invited Talk – (Renesas)				
	Venue: Anna Auditorium				
11 AM to 11. 30 AM	Networking Break				
	Venue: CS Hall				
11:30 AM to 1 PM	Design Contest, Venue: Kamaraj Auditorium				
11.30 AM to 1 PM	Track 1 (Embedded Systems Design)	Track 2 (VLSI Circuits and System Design)	Track 3 (VLSI Circuits and System Design)	Track 2 (Testing and Verification)	
	Venue: TT312	TT727	Ambedkar Auditorium	TT Gallery 2	
1 PM to 2 PM	Venue: FOODYS Networking Lunch				
2 PM to 03:30 PM	Track 4 (VLSI Circuits and System Design)	Track 5 (VLSI Circuits and System Design)	Track 2 (Emerging Devices and Material Technologies)	Track 2 (Embedded Systems Design)	Track 3 (Emerging Devices and Material Technologies)
	Venue: TT312	TT Gallery-2	Ambedkar Auditorium	Kamaraj Auditorium	TT 727
03:30 PM to 04:00 PM	Tea Break				
04:00 PM to 05:00 PM	PANEL DISCUSSION on Future of Undergraduate Engineers in Indian Semiconductor Ecosystem – A Chance or A Hype				
05:00 PM to 5:30 PM	Valedictory				
5:30 PM- 07:00 PM	Exploring Vellore				

