



VIT[®]
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

**School of Electronics Engineering
(SENSE)**

**Department of Micro & Nano Electronics
Offers**

**Value Added Programme
on**

**Scripting Languages for
Electronic Design Automation**



**24th – 31st October 2023
8th – 23rd January 2024**

About Training:

A scripting language is programming language that tells the computer what exactly to do in terms of a specific script or a language. They not only make your work faster, but also can make your job easier! Scripting languages can range from highly domain specific languages to general purpose programming languages. Use of scripting language is not just limited a specific domain, it can be used in any programming environment which requires user to do a repeated manual effort, script automates.

Who Can Attend?

→ VIT Students – PG students of VLSI Design.

Key Benefits:

This course is designed to introduce engineers to the interesting field of electronics automation. The candidates who successfully undergo this training would be able to design frontend as well as backend VLSI circuits. Programmers use scripting languages to enhance an app's or website's visual display by adding features and colors. Individuals can learn to code using scripting language without an extensive tech background.

Last date for Registration:

On or before : 20th October 2023

Course Fee:

Registration Fee: VIT Student: **Rs.500/-**
(Inclusive of GST)

Certificate, Course Material and Software will be provided.

Course Content:

- Introduction to Linux
- Usage of Linux Kernel and Basic Commands
- Directory and Users, Searching a file and Directory
- History and concepts of PERL, Scalar Data
- Arrays and List data – Control structures
- Regular Expressions, Directory Access
- Process Management – Packages and Modules
- An overview of TCL and Tk-TCL Language
- String Manipulations, Accessing Files, Controlling Tools
- Simulation Automation & Report Generation
- Intro to Python, Control Flow Tools,
- Regular Expressions, Input and Output- Errors and Exceptions, Netlist Patching

Convener:

Prof. S. Sivanantham
Dean, School of Electronics (SENSE)

Co-Convener:

Dr. Jagannadha Naidu K
HoD, Department of Micro & Nano Electronics

Coordinators:

- **Dr. Prayline Rajabai C,**
Assistant Professor (Senior)
- **Dr. Satheesh Kumar S,**
Assistant Professor (Senior)

For Registration contact:

Mr. V. Karthikeyan
Contact No.: 9894972399

Registration Link:

<https://events.vit.ac.in/>

Event Venue:

Gallery – II, Technology Tower