

**Two day Workshop**  
**on**  
**“SYSTEM DESIGN FOR IMAGEPROCESSING**  
**HANDS ON XILINX VIVADO & GOOGLE COLAB**

**9<sup>th</sup> & 10<sup>th</sup> November 2024**



**Organized by**



**School of Electronics Engineering (SENSE)**  
**Vellore Institute of Technology (VIT)**  
**Vellore-632014**

**Aim of the program**

- Introduction to FPGA Design flow  
Exploring IP catalogues and delve into block level design
- Exploring Python for image processing
- Familiarization to Cloud based platform for execution
- Familiarization to computer vision libraries
- Algorithm development, verification.
- FPGA implementation, Validation for applications like biomedical, agricultural etc..

**Who Can Attend?**

- Research scholars, PG and UG students.

**Course Content:**

- Introduction to Xilinx Vivado tool flow
- IP Integrator
- Block level Design
- Introduction to Python for images
- Open CV
- Numpy ,Matplotlib
- Hands on Google Colab
- Implementation of Image Processing algorithms on FPGA

Registration fee: Rs 300/- (Including GST)

**Chair Person:**

Dr.S. Sivanantham  
Professor & DEAN, SENSE

**Convenor:**

Dr.K. Jagannadha Naidu  
HOD, MNE, SENSE

**Co-Convenor:**

Dr. Noor Mohammed. V  
HOD,ECE ,SENSE

**Coordinators**

Dr. Sriadibhatla Sridevi,  
Professor

Dr. Rajeshkumar M  
Associate Professor

**Resource Persons:**

Dr. Dhayalakumar M  
Dhisai Technologies

Dr. Sriadibhatla Sridevi,  
Professor, VIT,Vellore

Dr Rajeshkumar M  
Associate Professor ,VIT,Vellore

Ms Dinah Ann Varughese  
ResearchScholar ,VIT,Vellore

**Last date for Registration:**

On or before : 30<sup>th</sup> October 2024

**Registration Link:**

**For paying Registration fee:**

**Visit : <https://events.vit.ac.in/>**