

About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

About the event:

The training program aims to deliver a solid foundation in antenna principles, highlight recent advancements, and present research outcomes related to 5G implementations. The FDP will feature sessions dedicated to technologies, techniques, and applications, designed to promote the exchange of knowledge and ideas among experts. It will serve as an excellent platform for those embarking on research in RF and antenna design.

Course Content:

- Introduction to CST Suite
- Horn Antenna and Reflector Antenna.
- Antenna Design
- Dual Patch and Feeding Network
- Filter Design
- Radiated Emission from Tx/Rx
- Equivalent Circuit Model.
- Filter design.
- Multimode Cavity Filter.

Resource Persons: Experts from Jyoti Electronics (CST Microwave Studio)

Eligibility: The training program is open to Industry personnel, Engineering Faculties, Research Scholars and UG/PG students.

Registration Fee :

Rs.700 + 18% GST (For Faculty and Students, Scholars),
Rs.1000 + 18% GST (For Industry persons),

Advisory Committee:

Dr. Sivanantham S

Professor and Dean,
School of Electronics Engineering (SENSE),

Vellore Institute of Technology, Vellore,
India.

Dr. Noor Mohammed V

Professor & Head,
Department of Communication
Engineering,
School of Electronics Engineering (SENSE),
Vellore Institute of Technology, Vellore,
India.

Coordinators

Dr. Rajesh N

Dr. M Karthikeyan

Department of Communication
Engineering,
School of Electronics Engineering (SENSE),
Vellore Institute of Technology,
Vellore – 632 014.
Contact Numbers: +91-9976254449
+91-8870458556

E-Mail: rajesh.natarajan@vit.ac.in

Karthikeyan.m@vit.ac.in

Registration: <https://events.vit.ac.in/>



Two Day Hands on Workshop

on

Design and Implementation of High Frequency RF Circuits and Antennas

(Hybrid Mode)

3rd & 4th January 2025

Organized by

**Department of Communication
Engineering**

School of Electronics Engineering

SENSE

**Vellore Institute of Technology
Vellore 632014**