School of Electronics Engineering

Department of Embedded Technology

Value Added Course

Advanced Embedded System Design(VAC1558)

Day 1 (20/01/25) 7.30PM-9.30PM

- Von-Neumann Vs Harvard Architectures, Unsigned Multipliers

Day 2 (21/01/25) 7.30PM-9.30PM

- Booth Multiplier, Unsigned Division algorithm

Day 3 (22/01/25) 11AM-1PM

- Cache Memory and Mapping Methods

Day 4 (22/01/25) 7.30PM-9.30PM

- EMI/EMC Definitions ,Component Selection

Day 5 (23/01/25) 7.30PM-9.30PM

- Grounding, Filtering

Day 6 (24/01/25) 7.30PM-9.30PM

- PCB Basics

Day 7 (05/02/25) 7.30PM-9.30PM

- Impedance Control, Power Distribution

Day 8 (10/02/25) 7.30PM-9.30PM

- Mother board Design Rules

Day 9 (11/02/25) 7.30PM-9.30PM

- Introduction to Computer Architecture

Interaction ONLINE : MS Team & OFFLINE: TT 603

Day 10 (12/02/25) 7.30PM– 9.30PM

- Introduction to RISC-V

Day 11 (14/02/25) 7.30PM-9.30PM

- Introduction to VEGA Processors

Day 12 (02/03/25) 9:30AM-5:30PM (TT603)

- Basics of ARIES board Programming
- Demo & Video Presentations
- Hands-on training using ARIES development boards

Day 13 (05/03/25) 11.00AM-1.00PM (TT603)

- Hands-on Training: Sensor Modules and Experiments
- IoT application development

Eligibility : UG/PG students of VIT,Vellore

Registration fee: **Rs. 250/(Including GST)**

Registration Link: https://events.vit.ac.in/

(This VAC is only for 50 Students on First Cum First served basis)

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