VIT®
Vellore Institute of Technology
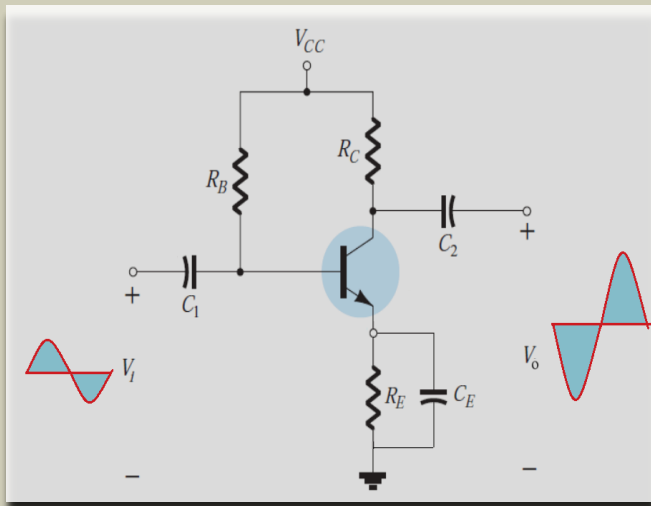(Deemed to be University under section 3 of UGC Act, 1956)

A Value Added Programme

On

Value-Added Program: Hands-on Analog & Digital Circuit Design

Dates: 09-02-2025 to 09-03-2025

Organized by

**School of Electrical Engineering
Vellore Institute of Technology,
Vellore-632014.**

ABOUT THE VAP

The main objective of this programme is to enable the participants to have an in depth understanding and hands-on training related to several applications with ORCAD software tools related to electrical circuit analysis, and diagnosis. Since the VAP aims at equipping the participants with a wide gamut of tools, the programme spans for a period of 30 hours from 9th February to 9th March 2025. This VAP helps student to learn the fundamental analysis of digital and analog circuits, simulation of various analog circuits, design of digital circuits and hardware implementation of analog and digital circuits. The outcome of the VAP aims at providing comprehensive overview of designing and implementation of analog and digital circuits and understanding of the software tool and its need for the assessment of circuits and its diagnosis.

TECHNICAL SESSIONS

- Fundamental analysis and simulation of ANN and Fuzzy Logic in MATLAB.

- Design and implementation of Digital Circuits.

- Hardware implementation of simple DC-AC circuits.

- Fundamental analysis of Analog Circuits.

- Hardware implementation of Analog and digital circuits.

- Design and development of Supervised Machine Learning Algorithms.

- Design and development of Unsupervised Machine Learning Algorithms.

ABOUT VIT

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spurs us on in our relentless pursuit of excellence. In fact, it has become a way of life for us. The highly motivated youngsters on the campus are a constant source of pride. Our Memoranda of Understanding with various international universities are our major strength. They provide for an exchange of students and faculty and encourage joint research projects for the mutual benefit of these universities. Many of our students, who pursue their research projects in foreign universities, bring high quality to their work and esteem to India and have done us proud. With steady steps, we continue our march forward.

ABOUT THE SCHOOL OF ELECTRICAL ENGINEERING

The School of Electrical Engineering (SELECT) has over 98 faculty members who pursued their UG, PG and Doctoral degrees from top-notch universities. The school offers B.Tech. (Electrical and Electronics Engineering), B.Tech. (Electronics and Instrumentation Engineering), B.Tech. (Electrical and Computer Engineering), M.Tech. (Power Electronics and Drives), M. Tech. (Control and Automation), Ph.D. and Integrated Ph.D. in Engineering. Both B.Tech. and M.Tech. programmes attract the Intelligent students from the country and abroad. The B.Tech. (Electrical and Electronics Engineering) and B.Tech. (Electronics and Instrumentation Engineering) Programmes are accredited by the Engineering

Accreditation Commission of ABET. All UG & PG programmes of the school are accredited by the Institution of Engineering and Technology (IET), U.K. The placement record of the school has always been impressive. Almost 100% of the student secure job from the campus placement and many of them are recruited in core companies. Danfoss Advance Drives Lab, Schneider Electric Smart Energy Monitoring Lab, Fluke Testing and Calibration Lab, Q-Max Automated Test Engineering Lab (Alumni Sponsored Lab) and NxP Semiconductors have established Centre of Excellence for students R&D activities under the guidance of faculty members and industry experts.

Resource Persons

**Dr. S Albert Alexander
SELECT, VIT**

**Dr. Sonam Shrivastava
SELECT, VIT**

REGISTRATION

Registration fee   : Rs.200/-

* Registration fee excludes 18% GST.

Prospective participants are requested to register for the VAP through the following web link

https://events.vit.ac.in/

IMPORTANT DATES

Last date for registration: 09-02-2025

Dates of VAP Classes
09-02-2025
16-02-2025
23-02-2025
03-03-2025
09-03-2025

Co-ordinators

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