Vellore Institute of Technology, Vellore

School of Electrical Engineering
Jointly Organizes with
School of Computer Science and Engineering
& School of Mechanical Engineering

VIT
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

Sheffield Hallam University

One-day International Workshop on (Online)

Industrial Applications of Artificial Intelligence

Coordinators

Dr. V. Indragandhi, SELECT
Dr. V. Subramaniyaswamy, SCOPE
Dr. B. Ashok, SMEC

Registration

Date:
13.03.2025
Time:
9.30 AM-4.30 PM

Sponsored by

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Dr.Subramaniyaswamy V, Prof ., SCOPE

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ABOUT VELLORE INSTITUTE OF TECHNOLOGY

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spur us on in our relentless pursuit of excellence. In fact, it has become a way of life for us. The highly motivated youngsters on the campus are a constant source of pride. Our Memoranda of Understanding with various international universities is our major strength. They provide for an exchange of students and faculty and encourage joint research projects for the mutual benefit of these universities. Many of our students, who pursue their research projects in foreign universities, bring high quality to their work and esteem to India and have done us proud. With steady steps, we continue our march forward. We look forward to meeting you here at VIT.

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About workshop

The "Industrial Applications of Artificial Intelligence" workshop covers key AI concepts, machine learning techniques, and real-world applications. Specifically, it delves into how AI revolutionizes the semiconductor industry by enhancing design, manufacturing, and quality control. It covers AI-driven techniques for chip design optimization, predictive maintenance in fabrication plants, defect detection, and yield improvement. Participants will explore real-world case studies and hands-on applications of machine learning and deep learning in semiconductor production. This workshop is ideal for engineers, researchers, and industry professionals looking to leverage AI for innovation and efficiency in semiconductor manufacturing.

Registration

- ✓ Participants provided with a certificate
- ✓ Attracting Prizes to Quiz Winners (End of Every Session)
- ✓ **No registration fee for participants**

Registration Link
https://forms.gle/ZeV99KGM4s7G8Ghe9

Date: 13.03.2025 Time: 9.30 AM-4.30 PM

Event Details

Technical Sessions

**Schedule will be communicated*

Contact

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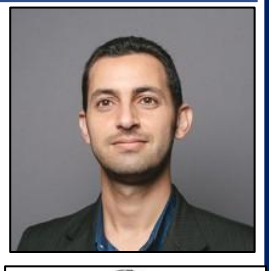
Speakers



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