



VIT[®]
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)



Industry Academia Conclave 2025

Electronics Renaissance: Accelerating Indian
Leadership for Global Sustainability

01 - 03 September 2025

Organised by
School of Electronics Engineering

**Pre-Conclave
Events**

24 & 31 August 2025

Technical Sponsors



About VIT

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of Teaching and Research spurs us on in our relentless pursuit of excellence. Our Memoranda of Understanding with various International Universities are our major strength. They provide for an exchange of Students and Faculty and encourage joint Research projects for the mutual benefit of these Universities. With steady steps, we continue our march forward. We look forward to meeting you here at VIT.

School of Electronics Engineering (SENSE)

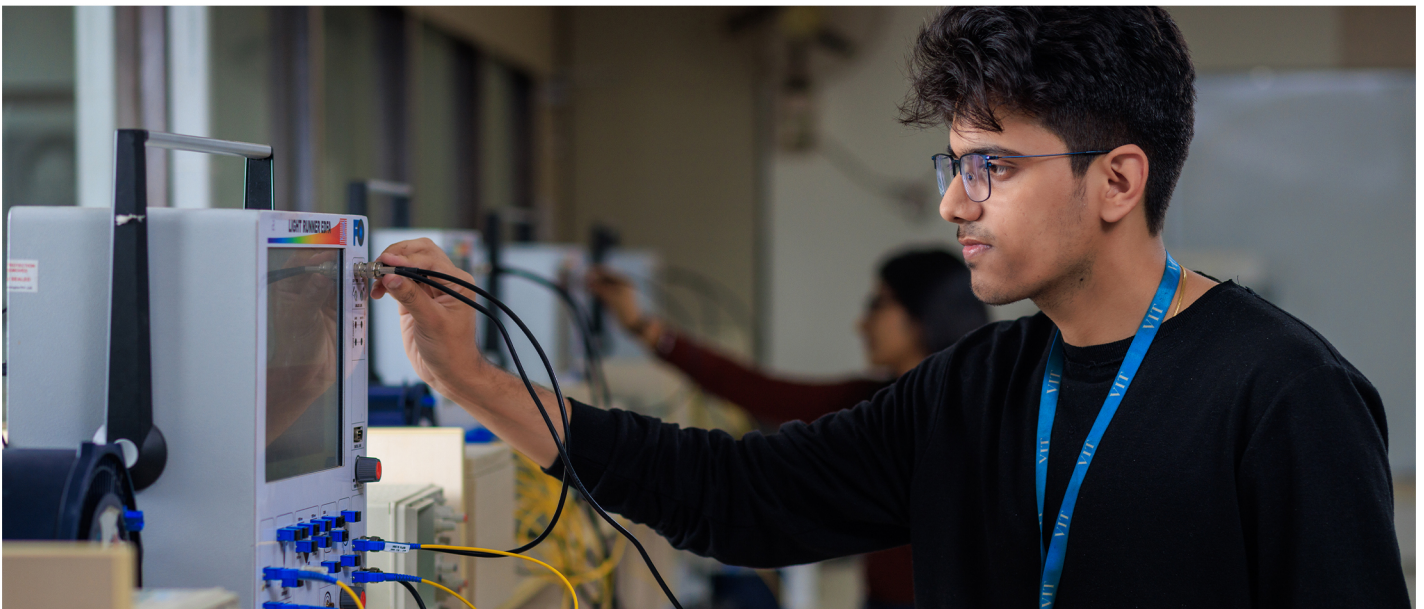
SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. The latest simulation tools are used to cater to various specializations and are equipped with facilities for measurement, characterization and synthesis of experimental as well as theoretical results. The school has many Industries sponsored advanced laboratories for carrying research and development. MoUs with many Foreign Universities, Research Organizations and Industries facilitate Student and Faculty exchange.

About Industry-Academia Conclave 2025

The School of Electronics Engineering of VIT is proud to host the prestigious Industry-Academia Conclave 2025, a dynamic forum designed to foster innovation, collaboration, and knowledge exchange between academia and Industry. This landmark event brings together all four specialized Departments-Communication Engineering, Embedded Technology, Micro & Nanoelectronics, and Sensor & Biomedical Technology in a joint effort to showcase cutting-edge advancements and forge impactful partnerships.

Over the course of Conclave, attendees can explore a wide range of opportunities/events including hands-on workshops, immersive training sessions, technical talks by leading industry experts, panel discussions with thought leaders, and a highly anticipated Hack-a-thon, the Conclave serves as a Memorandum of Understanding (MoU) signings to formalize collaborations with VIT in research, technical, development, and beyond.

This event not only celebrates innovation but also paves the way for transformative academia-industry synergies that will drive future technologies join us in shaping tomorrow's breakthroughs-today.



Day 1: 01 September 2025, Monday

Department of Communication Engineering

iAntenna'25 – Intelligent Antenna Design Contest & Workshop: 24 & 31 August 2025

Technical Sessions

- **Product Overview - Residential Gateways:** Mr. Ashok Govindarajan, Head of Solutions, Wireless Networks, Zilogic Systems
- **Sustainable Futures: India's Electronics Renaissance:** Mr. Sudheer Hundi, Director, Head of Network One OAM Team, Samsung R&D Institute India – Bangalore.
- **Wi-Fi- The invisible backbone of Modern Electronics:** Mr. T Sri Siva Charan-Senior Software Engineer, Tessolve Semiconductors Pvt. Ltd.
- **Orthogonal Time Frequency Space (OTFS): The Future Modulation Scheme for 6G:** Mr. Vasanth Kumar-Scientist-SE, UR Rao Satellite Centre, ISRO.
- **Wings of Ambition: My Journey into the World of Drones:** Mr. Adarsh V Srinivasan, CEO, SKYX Aerospace Pvt. Ltd.,
- **Intelligent Antenna Design:** Mr.R.Kannan, CEO, Quatek Technologies Pvt. Ltd.

MoU Signing

Panel Discussion - Beyond the Hype: Will India Truly Lead the 6G Era?

iAntenna'25 – Intelligent Antenna Design Contest & Workshop

Hands-on Workshop on

- **Python as a tool for AI Industry:** Mr. Muhammed Raees PC, Founder & CTO, Edutecnica Pvt. Ltd.
- **Test Automation framework and Application in Wi-Fi:** Mr. Ashok Govindarajan - Head of Solutions - Wireless Networks, Zilogic Systems
- **AI on the Edge: Exploring Hardware Accelerators for ML and DL:** Mr. Muhammed Raees PC, Founder & CTO, Edutecnica Pvt. Ltd
- **Industrial IoT Application:** Mr. C. Srinivas, Technical Lead-CST, Enthu Technology Solutions Pvt. Ltd., Coimbatore.

Contact: Dr. Mugelan R. K – mugelan.rk@vit.ac.in

Participating Industries



Day 2: 02 September 2025, Tuesday

Department of Embedded Technology

Technical Sessions

- **Tsunami Warning Systems** - Dr. Tata Sudhakar, Scientist – G (Retd.), NIOT, Chennai & Member, Naval Research Board (NRB), DRDO.
- **Transforming Weather Forecasting with AI and HPC Capabilities** - Dr. Athiyaman Balakrishnan, Retd. Scientist-F(Retd.), National Centre for Medium Range Weather Forecasting (NCMRWF), Ministry of Earth Sciences, Uttar Pradesh.
- **Shaping Future Engineering with 3D Experience:** Mr. Syed Ibrahim Abdul Rajak, Regional Manager, Advanced Reality Solutions, New Delhi.
- **Autonomous Mobile Robot:** Mr.V.Chandrasekar, Managing Director, Autonomous Robotics Solutions LLP, Chennai.

Hands-on Workshop on

- **Leverage AI in Software Defined Vehicles (SDVs):** Mr. Thiagarajan Subramani, Engineering Lead, DGM, Continental, Bengaluru.

24 Hour Hackathon resulting in Internship offers:

- Industrial Design & Animations
- Vulture Lines Tech Management Private Limited
- OneYes InfoTech Solutions Private Limited
- Thozhil

Contact: Dr. Sujatha R - sujatha.r@vit.ac.in

Broad Themes:

- 5G Communication
- Embedded Systems
- IoT & Sensors
- AI/ ML
- Wearable Biomedical devices
- Nano Electronics
- Robotics
- Automotive Electronics
- Smart Cities

Participating Industries



Day 3: 03 September 2025, Wednesday

Department of Micro and Nano Electronics (VLSI)

Technical Sessions

- **Navigating the Challenges of 3D ICs and Heterogeneous Integration:** Dr Vinay Kotha, DFT Manager, CISCO, USA.
- **RISC-V Verification:** Mrs. Lavanya Jagadeeswaran, CEO and Founder, Vayoma Systems.
- **Leveraging AI-Driven DFT:** Mr. Murugeswaran Surulivel, Associate Vice President DFT - Insemi Technology Services Pvt Ltd.
- **Overview and Application of Machine Learning Techniques in IC Design:** Mr. Lakshmanan Balasubramanian, Senior Member Technical Staff, Texas Instruments.
- **Scaling High-Performance Teams for Global Competitiveness and Sustainable Innovation:** Ravishankar Karthikeyan, Senior Director of Engineering, Tenstorrent.
- **Driving Yield and Sustainability through Data-Driven Process Integration and Advanced Manufacturing Analytics:** Dr Jagadish, Principal Engineer, Global Foundries.
- **Hands-on Workshop on**
Automatic Digital IC Design from RTL to GDS II using the OpenROAD platform.
- **Panel Discussion:** Road Map for talent pool creation for Semiconductor Industry

Contact: Dr. Jayakrishnan P – pjayakrishnan@vit.ac.in

Participating Industries



Day 3: 03 September 2025, Wednesday

Department of Sensor & Biomedical Technology

Technical Sessions

- **AI and Gen AI:** Mr. Sivaraman, Associate Director, AI, Verizon, Chennai.
- **Beyond the Lab:** Next-Gen Electrochemical Sensing with Palm Sens: Mr. Rajeev Das
Director, Class One Systems S&T Pvt. Ltd.
- **Advances in 3D Bioprinting:** Ms. Maria Da Costa, Biofabrication Specialist, NBIL.
- **Recent Trends in Hyper automation in Large Building Facilities using Low-Code Software, Internet of Things, and AI for Operational Excellence:** Mr. Ananda Padmanabhan, Founder CEO, Tenxhealth Technologies Pvt. Ltd., Coimbatore.
- **AI in Medical Imaging -Transformations at the Intersection of Technology and Healthcare:** Mr. Anand, 5C Network.
- **Innovating Healthcare -Human Centric Medical Devices:** Mr. Kumar, TMI systems, Bangalore.
- **Hydrogen Fuel Generation:** Mr. Jeya Kumar V, Managing Director, Mr. Nagarajan. R – Technical Director, Mr. Abhisekh Kumar. J.V – Executive Direct, Tiaano Fabricators Pvt. Ltd., Chennai

Project Expo

Startup Hitching

24 Hours Hackathon

Contact: Dr. Jeeva J.B – jbjeeva@vit.ac.in

Participating Industries



Who can attend?

- Diploma Students, UG Students & PG Students
- Research
- Faculty Members
- Industry Professionals

Scholars

Time: 09.00 AM – 05.00 PM(IST)

Stall (Sponsorship) Charges

- ₹11,800 per day per stall (inclusive of 18% GST)

Registration:

- <https://events.vit.ac.in/>

Participation Fees

- ₹1062 for all 3 days (inclusive of 18% GST)
- ₹354 for any specific event/one day (inclusive of 18% GST)

Note

- Participants registering for all three days will have access to all events, with no separate registration needed for Workshops or Hackathons.
- Those registering for a specific event may also attend other events scheduled on the same day without additional registration.

Last Date for Registration: 30th August 2025

General Chairs

- Dr. Jasmin Pemeena Priyadarisini M Dean, SENSE, Vellore Institute of Technology, Vellore
- Dr. Sakthivel R, Associate Dean, SENSE, Vellore Institute of Technology, Vellore

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- Dr. Kannadassan D, Vellore Institute of Technology, Vellore
- Dr. Sundar S, Vellore Institute of Technology, Vellore
- Dr. Sri Adibhatla Sridevi, Vellore Institute of Technology, Vellore

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- Dr. Vidhya S, Vellore Institute of Technology, Vellore
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- Dr. Arunkumar Chandrasekhar, Vellore Institute of Technology, Vellore

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- Dr. Bijaylaxmi Das

Alumni Connect/Refreshments

- Dr. Muthuraja S

Placement/Internship Chairs

- Dr. Jayakrishnan P
- Dr. Shilpi Ruchi Kerketta



Key Highlights

- 23 Technical Sessions
- 2 Panel Discussions
- Two 24 Hours Hackathon
- Internship Offers for best team
- 7 Hands-on Workshops
- 10 MoU Signing
- Design Contest
- Pre Conclave Events
- Project Expo
- Startup Pitching
- 35+ Industries in Collaboration
- 40+ Industry Experts visiting VIT Vellore

For more details

<https://vit.ac.in/conference/IAC-2025/>



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