

About SENSE:

SENSE at VIT was established to impart state-of-the-art knowledge in Electronics and Communication Engineering and related areas. The school has established laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded Systems, Sensors, and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

About the event:

OpenROAD platform. Learn to navigate the complete RTL-to-GDSII flow, an automated process that transforms a high-level circuit description into a physical chip layout ready for manufacturing. We'll explore key concepts like logic synthesis, floorplanning, placement, and routing, giving you the practical skills needed to design your own integrated circuits. This session is tailored for students, researchers, and engineers who want to enter the world of chip design without the high cost of traditional software. We'll guide you through setting up the OpenROAD environment, running a complete design, and analyzing the results, empowering you to leverage the power of open-source tools for your next project.

Speaker:

Dr. Noor Mohammad SK

Associate Professor,
Department of Computer Science and Engineering,
Indian Institute of Information Technology Design and
Manufacturing (IIITDM)
Kancheepuram, Chennai - 600127



Eligibility: The workshop is open to Research Scholars and UG/PG students.

Registration Fee: Rs.300 + 18% GST

Registration: <https://events.vit.ac.in/>

Advisory Committee:

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Industry Academia Conclave'25

Theme: Electronics
Renaissance: Accelerating
Indian Leadership for Global
Sustainability



Workshops on

Automated Digital IC Design from RTL to GDSII using the OpenROAD Platform

2nd Sep, 2025