



## About VIT

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spur us on in our relentless pursuit of excellence. Our Memoranda of Understanding with various international universities are our major strength.

## About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

## About the event:

The training program aims to impart familiarization in Advanced Design System tool that caters to the needs of an industry from design to tape out of passive and active RF components. This program is designed to promote the exchange of knowledge and ideas among Keysight experts. It will serve as an excellent platform for those embarking on research and career in RF design.

## Course Content:

- ✓ Fundamentals of RF amplifier
- ✓ Design of an RF amplifier
- ✓ Hands on with ADS
  - RF Schematic design
  - Physical Layout design
  - Gerber file generation

## Resource Persons

- Experts from
- Keysight Technologies Pvt Ltd
  - Infinipoint Technologies Pvt Ltd

**Eligibility:** The training program is open to Research Scholars and UG/PG students.

## Registration Fee:

Rs.500 /- (including GST)

**Registration:** <https://events.vit.ac.in/>

## Advisory Committee:

**Dr. Jasmine Pemeena Priyadarisini**

Professor and Dean,  
School of Electronics Engineering,  
Vellore Institute of Technology, Vellore, India.

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## Coordinators:

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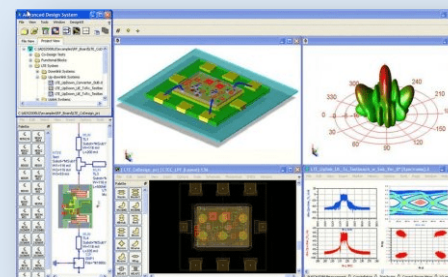
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## Hands on workshop



## Building the future with Advanced Design System (ADS)

13 - 17, Oct., 2025

(Time: 7:30 – 8:30 PM)

**TT145 – Microwave Lab**

**Technology Tower**

Organized by

**School of Electronics Engineering (SENSE)**  
**Vellore Institute of Technology, Vellore**

Welcome to Blended Learning