



About VIT

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spur us on in our relentless pursuit of excellence. Our Memoranda of Understanding with various international universities are our major strength.

About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

About the event:

The training program's goal is to familiarize participants with the CST studio suite of tools, which can be used for anything from design to tape-out of passive and active radio frequency components. The goal of this program is to encourage professionals to share ideas and information. It will be a great starting point for anybody pursuing research and a career in radio frequency design..

Course Content:

Session I – Fundamentals

10:00 AM – 1:00 PM

- Introduction to CST Studio Suite
- Fundamentals of Antenna Design for Next-Gen Wireless Systems
- Overview of Meta-surfaces and their applications

Lunch Break: 1:00 PM – 2:00 PM

Session II – Practical Session

2:00 PM – 5:00 PM

- Antenna Design and Simulation using CST
- Metasurface Unit Cell Design and Analysis
- Q&A and Discussion

Advisory Committee:

Dr. Jasmine Pemeena Priyadarisini

Professor and Dean,
School of Electronics Engineering,
Vellore Institute of Technology, Vellore, India.

Dr. Kannadassan D

Professor and Head,
Department of Communication
Engineering, School of Electronics
Engineering, Vellore Institute of Technology, Vellore,
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Coordinators:

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Resource Persons

Dr. K. G. Sujanth Narayan Ph.D
Application Engineer,
Sinetec Technologies.

His technical expertise spans antenna and metasurface design, RF system development, GNSS and V2X communication, and next-generation (5G/6G) wireless technologies. He is proficient in EM simulation and design tools such as CST Studio Suite, Ansys HFSS, Keysight ADS, EMPro, and MATLAB.

Eligibility: The training program is open to Research Scholars and UG/PG students.

Registration Fee:

Rs.200 +(GST)

Registration:

www.events.vit.ac.in



*One day Workshop
on Antenna &
Metasurface
Design for Next-
Gen Wireless
Applications.*

5th November, 2025

(Time: 10:00 AM – 5:00 PM)

(Hybrid Mode)

**TT145 – Microwave Lab
Technology Tower**



Organized by

School of Electronics Engineering (SENSE)

Vellore Institute of Technology, Vellore