



**Value-added Course
on
Electromagnetic
Interference and
Compatibility (EMI/EMC):
Basics and Standards
(VAC2338)**

10th Jan. 2026 to 22nd Feb. 2026

(32 hours)

Organized by
**Department of Communication
Engineering
&
Department of Micro and
Nanoelectronics**

**School of Electronics Engineering
(SENSE)**

Vellore Institute of Technology

Vellore-632014

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About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

About the event:

The primary goal of this Value-Added Course was to provide students with fundamental knowledge of Electromagnetic Interference (EMI) and Electromagnetic Compatibility (EMC) critical aspects in the design and operation of modern electronic systems. The course aimed to familiarize participants with the sources, effects, mitigation techniques, and international standards governing EMI/EMC compliance in industrial, automotive, aerospace, and communication systems.

Course Content:

- Fundamentals overview of Electromagnetic interference and Electromagnetic Compatibility
- Importance of Addressing EMC Issues Early
- Essential elements of an EMC problem
- EMI Mitigation Techniques
- Industrial importance of EMC/EMI
- Introduction to EMI/EMC 3D PCB Simulations
- 3D Simulation of PCB Antenna
- 3D Simulation of Electromagnetic exposure to human body (SAR)
- Step to be followed in Measurement of EMC / EMI

Eligibility: UG/PG Students and Research Scholar of VIT.

Mandatory for Certificate:

- **Minimum 50% Marks in Assessment Test**
- **Minimum 75% attendance**

Registration Fee:

Rs.200 + GST (For UG/PG Students),
Rs.300 + GST (For Research Scholars),

Payment Link: <https://events.vit.ac.in/>