

## CORE COMMITTEE MEMBERS

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### Vellore Institute of Technology (VIT)

VIT was established to provide quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education consistently. The campus has a cosmopolitan atmosphere with students from all corners of the globe. At VIT, we prioritize student growth, empowering them through the guidance of highly experienced and knowledgeable educators. Our commitment to international standards in teaching and research fuels our pursuit of academic excellence.

### About NetSim

NetSim is a powerful network simulation and emulation tool used for modeling, analyzing, and visualizing communication networks. It supports 5G/6G, IoT, MANETs, and more, enabling protocol development, performance testing, and real-time hardware integration.

## Resource Person

**Mr. Vishal Bhat**,  
**Sr. Application Engineer, NetSim. Bengaluru**

### Advisory Committee

**Dr. Jasmin Pemeena Priyadarsini M**  
**Professor HAG & Dean**  
**School of Electronics Engineering, VIT -Vellore**  
**Dr. Thangaraja J**  
**Professor & Director,**  
**Automotive Research Centre, VIT -Vellore**  
**Dr. Kannadassan. D**  
**Professor & Head,**  
**Dept. of Communication Engg, VIT -Vellore**

### Workshop Benefits

- Hands-on experience with NetSim's GUI and XML-based configuration tools.
- Learn to design and simulate networks using drag-and-drop interfaces.
- Run Discrete Event Simulations (DES) and analyze packet-level behavior.

### Topics covered

- Introduction to NetSim
- NetSim: AI/ML for Communication Networks
- 5G/6G RAN Simulation and Hands on experience
- V2V, V2I and V2X Communication using VANETs and other external tools
- NetSim interfacing with third party tools
- How can NetSim contribute for Research and Development
- Wireless Technologies that are covered in NetSim.



# VIT®



**One day FDP/ Workshop on  
Network Simulator (NetSim)  
Hybrid Mode(Offline for VITians)**

**Venue : T134**

**Organized by  
School of Electronics Engineering (SENSE) &  
Automotive Research Centre ( ARC)**

**1st Nov 2025 (Saturday)**

**Time : 10.00 AM - 5:00 PM**



## Registration

**Registration Fee and Limit : Rs. 200**

**Registration Link: <https://events.vit.ac.in>**

**Coordinators:**

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