

**VELLORE INSTITUTE OF TECHNOLOGY, VELLORE
SCHOOL OF ELECTRICAL ENGINEERING**

ORGANIZES

TWO DAY TRAINING

ON

**SEMICONDUCTOR CIRCUIT DESIGN
AND SIMULATION**

SPONSORED BY



VENUE

DATE AND TIME

REGISTER NOW

**HANDS-ON TRAINING
WITH CADENCE TOOLS
AT
TT128 LAB**

**10.01.2026 &
11.01.2026
9 AM TO 5PM**



VIT®
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

ORGANIZING COMMITTEE

CHIEF PATRON

DR. G. VISWANATHAN

HONORABLE CHANCELLOR

PATRONS

SHRI SANKAR VISWANATHAN, VICE PRESIDENT

DR. SEKAR VISWANATHAN, VICE PRESIDENT

DR. G. V. SELVAM, VICE PRESIDENT

DR. SANDHYA PENTAREDDY, EXECUTIVE DIRECTOR

MRS. KADHAMBARI S VISWANATHAN, ASST VICE-PRESIDENT

DR. V. S. KANCHANA BHAASKARAN, VICE CHANCELLOR

DR. PARTHA SHARATHI MALLICK, PRO-VICE CHANCELLOR

DR. T. JAYABARATHI, REGISTRAR

ORGANIZING CHAIR

DR. M. KOWSALYA

DEAN, SCHOOL OF ELECTRICAL ENGINEERING

ORGANIZING CO-CHAIR

DR. N. RUBAN

ASSOCIATE DEAN, SCHOOL OF ELECTRICAL ENGINEERING

ADVISORY COMMITTEE

DR. YEDULA PEDDA OBULESU

HOD, ENERGY AND POWER ELECTRONICS

DR. VINODH KUMAR E

HOD, CONTROL AND AUTOMATION

DR. P. VIJAYAPRIYA

HOD, ELECTRICAL ENGINEERING

DR. P. MAHALAKSHMI

HOD, INSTRUMENTATION ENGINEERING

CONVENOR

DR.V.INDRAGANDHI & DR. MARIMUTHU R , SELECT

CO-ORDINATORS

DR.B.ASHOK/SMEC AND DR.V.SUBRAMANIASWAMY/SCOPE

ABOUT VIT

VIT was founded in 1984 as Vellore Engineering College by the Chancellor, Dr. G. Viswanathan. From its humble beginnings, the institution has grown exponentially to more than 35,000 students. It was conferred the University status in 2001 in recognition of its excellence in academics, research, and extracurricular initiatives. Currently, VIT has 6 campuses – in Vellore, Chennai, Amaravati (AP), Bangalore, Bhopal (MP), and Mauritius. VIT has been consistently ranked among the best institutions of the country and is aspiring to emerge as a global leader. The National Institutional Ranking Framework (NIRF) of the MHRD, Government of India, has identified VIT as the best Private Engineering Institution in India. VIT holds an exemplary placement record by consistently placing more than ninety percent of the students in good companies. The VIT's international linkages provide ample opportunities for students and faculty to gain global exposure. VIT alumni, spread across the world, are serving the most advanced as well as the most deprived.

ABOUT SELECT

School of Electrical Engineering (SELECT) has 98 faculty members who have done their UG and PG degrees from the top-notch universities. The school offers B.Tech. (Electrical and Electronics Engineering), B.Tech. (Electronics and Instrumentation Engineering), B.Tech. (Electrical and Computer Science Engineering), M.Tech. (Power Electronics and Drives) and M. Tech (Control and Automation), M.S. by Research, and Ph.D. in Engineering. B.Tech (Electrical and Electronics Engineering) and B.Tech. (Electronics and Instrumentation Engineering), is accredited by the Engineering Accreditation Commission of ABET and also by NBA for six years. All UG & PG programmes of the school are accredited by the Institution of Engineering and Technology (IET), the U.K. The placement record of the school has always been impressive. Almost 100% of the students get jobs from the campus placement, and many of them are getting them in core companies every year. The school has state-of-the-art laboratories in almost all areas of Electrical, Electronics, and Instrumentation Engineering. Every year, students get scholarships to do their final year projects abroad under the Semester Abroad Program (SAP).

TOPICS TO BE COVERED

- CMOS Logic Simulation Using Cadence Virtuoso
- Hands-On Different Logic Simulation - CMOS
- Parametric Analysis, Power estimation - Cadence Virtuoso
- Delay Estimation & Performance Metrics - Cadence Virtuoso
- Introduction to Verilog HDL
- RTL Simulation and Synthesis Using Cadence tools (Nclaunch + genus)
- Placement and CTS Using Cadence Innovus
- Complete Physical design using cadence Innovus

HOW TO APPLY?

For registration, use the following link:
<https://forms.gle/8K87feu6pwsuS3e6>

INSTRUCTIONS FOR PARTICIPANTS

- Only Undergraduate (UG) students are eligible to register.
- College ID card is mandatory for all participants throughout the programme.
- Attendance is compulsory for every session.
- A quiz will be conducted at the end of the training programme.
- Feedback must be submitted at the end of each session.

CONTACT DETAILS

Dr.Indragandhi V
Professor , School of Electrical Engineering
Vellore Institute of Technology
Vellore 632 014, Tamil Nadu
Contact Number: 9750603539
E-mail: indragandhi.v@vit.ac.in