

### **About VIT**

Vellore Institute of Technology was founded in 1984 as Vellore Engineering College by the Chancellor Dr. G. Viswanathan. From its humble beginning, the institution has grown exponentially to that of having more than 36,000 students. Students from all the states of India and from more than 60 countries are studying at VIT. University status was conferred in 2001 by MHRD Govt. of India in recognition of its excellence in academics, research and extracurricular initiatives. Currently, VIT has 4 campuses – in Vellore, Chennai, Amaravati (AP) and Bhopal (MP). VIT is recognized as an Institution of Eminence (IoE) by MHRD, Government of India. VIT has been ranked 699 among best global universities, 207 among best global universities in Asia, 7 in best global universities in India.

VIT has gone for accreditation by NAAC [India], IET [UK], and ABET [USA] and follows world class academic processes. VIT EEE department has gone for NBA accreditation this year and secured 7 years of accredited status which is a maximum for programmes offered by deemed universities

VIT is the first and only University in India to get 4 star rating from QS, the world universities ranking organization. VIT has also been ranked in the top 601-800 in Times Higher Education (THE) World University Ranking (Emerging Economies) in 2025. VIT has been ranked 396<sup>th</sup> in the world and 8<sup>th</sup> in India.

### **About School of Electrical Engineering**

School of Electrical Engineering (SELECT) has 93 faculty members who have done their UG and PG degrees from the top-notch universities. The School offers B.Tech (Electrical and Electronics Engineering), B.Tech (Electronics and Instrumentation Engineering), M.Tech (Power Electronics and Drives) and M. Tech (Control and Automation), M.S. by Research and Ph.D. in Engineering. B.Tech (Electrical and Electronics Engineering) and B.Tech (Electronics and Instrumentation Engineering), is accredited by the Engineering Accreditation Commission of ABET. All UG & PG programmes of the school are accredited by the Institution of Engineering and

Technology (IET), U.K. The placement record of the School has always been impressive. Almost 100% of the students get job from the campus placement and many of them are getting it in core companies every year. The School has state-of-the art laboratories in almost all the areas of Electrical, Electronics and Instrumentation Engineering. Every year, students get scholarships to do their final year projects abroad under the Semester Abroad Program (SAP). Schneider Electric, India and NXP Semiconductors, India, have established Centre of Excellence for students R&D activities under the guidance of faculty members and Industry experts. The School has signed MoUs with many foreign Universities, research organizations and Industries from where students get benefits for their R&D Work / Projects from the MoUs.

### **About this Program**

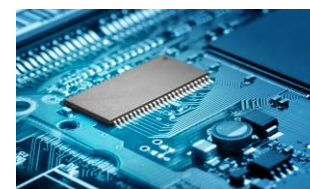
The uses of embedded systems are virtually limitless, because every day new products are introduced to the market that utilize embedded computers in novel ways. In recent years, hardware such as microprocessors, microcontrollers and FPGA chips have become much cheaper. So when implementing a new form of control, it's wiser to just buy the generic chip and write your own custom software for it. Producing a custom-made chip to handle a particular task or set of tasks costs far more time and money. Many embedded computers, even come with extensive libraries, so that "writing your own software" becomes a very trivial task indeed. The objective of this workshop is to present and discuss the embedded system architecture, to write the software for the processor, debug the software, to interface peripherals with the processor. (Hands on training will be provided)

### **Topics to be covered**

- Introduction to Advanced Embedded Architecture
- Recent Developments in Embedded Architecture
- ARM Processor Interfacing
- Hands on with ARM Processor



## **One Day Workshop On Embedded Systems Architecture and ARM Processor – Hands on 30<sup>th</sup> , January 2026.**



### **Coordinators**

**Dr. P.Ponnambalam  
Dr. D.Rama Prabha**

*Organized by*  
**School of Electrical Engineering,  
Vellore Institute of Technology,  
Vellore, Tamilnadu**

VIT - A place to learn; A chance to grow

**One Day Workshop on  
EMBEDDED SYSTEMS ARCHITECTURE AND  
ARM PROCESSOR – HANDS ON**

**REGISTRATION FORM**

(Capital Letters only)

Full Name .....

Gender: Male / Female.

Designation: .....

Organization: .....

Address: .....

.....

.....

Mobile No.: .....

E-mail: .....

Amount: . . . . . Date: . . . . .

**Declaration:** The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the course and attend the course for the entire duration.

Place:

Date:

Signature of Participant

**Registration Fee:**

Faculty/Industry Experts/Scientists: **Rs. 1,000+GST**

Students/Research Scholars: **Rs. 500 +GST**

(Registration fee Includes a kit, working lunch and snacks. A certificate will be issued to all the registered participants.). Use following link to make online payment.

<https://events.vit.ac.in/>

**Important Dates:**

Last date for registration: **23<sup>rd</sup> January, 2026**

Workshop Date: **30<sup>th</sup> January, 2026**

Prospective participants are requested to fill up the registration form & pay fee in online mode and send it to the coordinator.

**Resource Persons**

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