

## ABOUT SENSE

The School of Electronics Engineering (SENSE) at VIT was established with the objective of imparting state-of-the-art knowledge in Electronics and Communication Engineering and its allied disciplines. The School is equipped with well-established laboratories featuring advanced infrastructure in the domains of Electronics, Communication, VLSI, Embedded Systems, Sensors, and Nanotechnology. The faculty members are actively engaged in research and development activities and are involved in sponsored research projects funded by reputed government agencies such as DRDO, ISRO (RESPOND), and DST.

## ABOUT THE EVENT

The primary objective of this Value-Added Course was to equip students with fundamental knowledge of Electromagnetic Interference (EMI) and Electromagnetic Compatibility (EMC), which are critical considerations in the design and operation of modern electronic systems. The course aimed to familiarize participants with the sources, effects, mitigation techniques, and international standards related to EMI/EMC compliance across industrial, automotive, aerospace, and communication applications.

## COURSE CONTENT

- EMC-Oriented System Design Principles
- PCB-Level EMI Mitigation Techniques Using Cadence Tools
- EMI/EMC Measurement Techniques and Standards
- EMI/EMC Measurement Practices

## WHO CAN ATTEND

This course is open to UG, PG students and Research Scholars of VIT.

## REGISTRATION FEE

**Rs. 1000 /- (excluding GST)**

## REGISTRATION LINK

<https://events.vit.ac.in>

## RESOURCE PERSONS

- **Mr. A.K.Swamy**, Dy. General Manager  
Tata Advanced systems Pvt. Ltd., Bengaluru
- **Mr. Rakesh B. R.**, Field Application Engineer  
Entuple Technologies Pvt. Ltd., Bengaluru
- **Mr. Ezhil Arasan Vinayagam**, Head of Operations  
Cosmic Test Lab Pvt. Ltd, Bengaluru

## ADVISORY COMMITTEE

Dr. Jasmin Pemeena Priyadarsini M, Dean / SENSE  
Dr. Sakthivel R, Asso. Dean / SENSE  
Dr. Kannadasan D, HoD/DCE  
Dr. Sundar S, HoD/DET

## COORDINATORS

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This Course is aligned with the following SDGs



**VIT**<sup>®</sup>  
Vellore Institute of Technology  
(Deemed to be University under section 3 of UGC Act, 1956)

## VALUE ADDED COURSE

on

**Electromagnetic Interference  
and  
Compatibility (EMI/EMC):  
Basics and Standards  
(VAC2338)**

**21, Feb., 2026 ~ 5, Apr., 2026**

**( 30 Hrs. )**

Organized by

**Department of Communication Eng.,**

&

**Department of Embedded Tech.,**

**School of Electronics Engineering  
Vellore Institute of Technology**

**Vellore - 632014**

