

## Organizing Committee

### Chief Patron

**Dr. G. Viswanathan**

Honorable Chancellor

### Patrons

**Dr. Sankar Viswanathan**, Vice President

**Dr. Sekar Viswanathan**, Vice President

**Dr. G. V. Selvam**, Vice President

**Dr. Sandhya Pentareddy**, Executive Director

**Ms. Kadhambari S. Viswanathan**, AVP

**Dr. V. S. Kanchana Bhaaskaran**, VC

**Dr. Partha Sharathi Mallick**, Pro-VC

**Dr. T. Jayabarathi**, Registrar

### Conveners

**Dr. M. Kowsalya**

Dean, School of Electrical Engineering

**Dr. Jasmin Pemeena Priyadarsini M**

Dean, School of Electronics Engineering

**Dr. N. Ruban**

Asso. Dean, School of Electrical Engineering

**Dr. Sakthivel R**

Asso. Dean, School of Electronics Engineering

### Advisory Committee

**Dr. Yedula Pedda Obulesu**

HoD, Dept. of Energy and Power Electronics

**Dr. Vinoth Kumar E**

HoD, Department of Control and Automation

**Dr. P. Vijayapriya**

HoD, Department of Electrical Engineering

**Dr. P. Mahalakshmi**

HoD, Dept. of Instrumentation Engineering

## ABOUT VIT

VIT was founded in 1984 as Vellore Engineering College by the Chancellor, Dr. G. Viswanathan. From its humble beginnings, the institution has grown exponentially to that of more than 35,000 students. It was conferred the University status in 2001 in recognition of its excellence in academics, research, and extracurricular initiatives. Currently, VIT has 6 campuses – in Vellore, Chennai, Amaravati (AP), Bangalore, Bhopal (MP) and Mauritius. VIT has been consistently ranked among the best institutions of the country and is aspiring to emerge as a global leader. The National Institutional Ranking Framework (NIRF) of the MHRD, Government of India, has identified VIT as the best Private Engineering Institution in India. With students from all the states of India and from more than 50 countries, the cosmopolitan VIT provides an appropriate ambience for holistic learning and comfortable living. Sports, games and cultural activities are an integral part of student life on campus. VIT holds an exemplary placement record by consistently placing more than ninety percentage of the students in good companies. The VIT's international linkages provide ample opportunities for students and faculty to gain global exposure. VIT alumni, spread across the world, are serving the most-advanced as well as the most deprived.

## ABOUT SELECT

School of Electrical Engineering (SELECT) has 84 faculty members who have done their UG and PG degrees from the top-notch universities. The school offers B.Tech. (Electrical and Electronics Engineering), B.Tech. (Electronics and Instrumentation Engineering), B.Tech. (Electrical and Computer Engineering), M.Tech. (Power Electronics and Drives) and M. Tech (Control and Automation) and Ph.D. in Engineering. B.Tech. (Electrical and Electronics Engineering) and B.Tech. (Electronics and Instrumentation Engineering), is accredited by the Engineering Accreditation Commission of ABET and also by NBA for six years. All UG & PG programmes of the school are accredited by the Institution of Engineering and Technology (IET), the U.K. The placement record of the school has always been impressive. Almost 100% of the students get job from the campus placement and many of them are getting it in core companies every year. The school has state-of-the-art laboratories in almost all the areas of Electrical, Electronics and Instrumentation Engineering. The school has signed MoUs with many foreign universities, research organizations and Industries from where students get benefits for their R&D Work / Projects from the MoUs.



# VIT®

Vellore Institute of Technology  
(Deemed to be University under section 3 of UGC Act, 1956)

## International Workshop on 'Future-Ready Electrical Machines: Innovations & Industry Insights'

**(FREM-2026)**

**23.02.2026 to 25.02.2026**

**(Hybrid Mode)**

### Coordinators

**Dr. Vijayapriya P**

**Dr. Saranya K C**

**Dr. Mageshvaran R**

### In coordination with

**Dr. Ramasamy Anbarasu**

**Honorary Professor**

**Brunel University**

*Jointly Organized by*

**SCHOOL OF ELECTRICAL ENGINEERING  
SCHOOL OF ELECTRONICS ENGINEERING**



## ABOUT SENSE

SENSE at VIT was established to impart state of the art knowledge in Electronics and Communication Engineering and related areas. The school has established laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded systems, Sensors and Nanotechnology. Faculties are involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO and DST.

## OBJECTIVES

This workshop aims to provide participants with advanced insights into next-generation electrical machine technologies and their emerging applications in clean energy, aerospace, grid stability, and electric mobility. The program is intended to bridge theoretical foundations with real-world engineering challenges, enabling participants to understand performance optimization, sustainability considerations, and future trends in advanced electrical energy systems.

## CONTENTS OF THE WORKSHOP

- ❖ Cryogenic and superconducting motors for hydrogen powered aircraft
- ❖ Trends in Aerospace Industry
- ❖ Industrial Permanent Magnet Machine – Design Consideration & Manufacturing Challenges
- ❖ Nuclear Powered Container ships
- ❖ Design Choices for Medium Speed PM Generators for Wind Turbines
- ❖ Battery Management systems
- ❖ Electric Propulsion in Drone Technology
- ❖ Advanced Power Conversion & Transportation System
- ❖ The Case for Modern Grid Inertia: Synchronous Condensers, Vacuum Flywheels & System Challenge

## RESOURCE PERSONS

- Dr. Suma M N, Technology Manager at GE Aerospace Research
- Mrs. Babita Gupta, Associate Director NPC, India
- Dr. Azhagar Raj Engineering Director, Epropelled, Chennai
- Mr. Amol Kolwalkar, GE Vernova Bangalore, Karnataka, India
- Prof. Sherley Pei, Professor of Transport Electrification, University of Bath
- Dr. Vipul Patel, Engineering Manager, GE Vernova, Rugby, UK
- Dr. Anoop Jaswal, Senior Specialist, Vestas Wind Systems A/S, Denmark
- Mr. Ganesh Kumar Mukilan, GL Tech Consulting Ltd, Coventry, UK
- Dr. Anshuman Tripathi, Sure Energy, Singapore
- Mr. Saravanan Rajagopalan, Engineer, Baker & Hughes, UK

## TARGET GROUP & MODE

**Target Group:** Assistant Professors/ Associate Professors/ Professors/ Ph.D. scholars/ PG and UG Students/ Industry Professionals

**Mode:** Online and Offline

## REGISTRATION DETAILS

Rs. 500 + 18% GST (Academicians / Industrialists)

Rs. 250 + 18% GST (UG / PG Students / Research Scholars)

Last date for registration : 20.02.2026

Last date for payment : 21.02.2026 \*

\* The payment link will be shared after completing the registration.

Shared bed accommodation may be provided on a payment basis. Contact the coordinators for further details.

## HOW TO APPLY?

For registration, use the following link:

<https://tinyurl.com/FREM-2026>



## CONTACT DETAILS

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