



VIT[®]

Vellore Institute of Technology

(Deemed to be University under section 3 of UGC Act, 1956)

School of Computer Science Engineering and
Information Systems

Industry-Ready

SYSTEM DESIGN

Value Added Course



System Design
Architecture



Machine Learning
System Design



Real World
Use Cases

23rd Feb 2026 to 25th March 2026
(5.30 PM to 7.30 PM)

Total 30 Hours of Training

Registration Fees : Rs. 300+GST

Last Date of Registration: 19.02.2026

*"Proficiency in System Design is often a prerequisite
for senior engineering and leadership roles."*