

## About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST

## Course Content:

- Introduction to requirement of 5G/6G wireless communication
- RF & Microwave Basics & Wireless Integration
- Design of microwave circuits for wireless technology using Industry Standards software
- Fabrication Using Mipecc PCB Prototyping Machine
- Practical Deployment Challenges in 5G and IoT Devices
- Hands-on Mini Project: Design and Testing of Sub-6 GHz/5G

Date: 20<sup>th</sup> June 2026

Venue: TT 145 (Hybrid)

Time: 10.00 AM to 5.00 PM

## Registration Fee:

UG/PG/Research Scholars// Faculty: **Rs. 200 +GST**

A certificate will be issued to all the registered participants. Use following link to make online payment.

<https://events.vit.ac.in/>

## Advisory Committee:

### Dr. Jasmine Pemeena Priyadarisini

Professor and Dean,

School of Electronics Engineering  
(SENSE), Vellore Institute of Technology,  
Vellore, India.

### Dr. Kannadassan D

Professor & Head,

Department of Communication  
Engineering, School of Electronics  
Engineering (SENSE), Vellore  
Institute of Technology, Vellore,  
India.

## Coordinators

### Dr. Shanidul Hoque

### Dr. Dilip Kumar Choudhary

School of Electronics Engineering  
(SENSE), Vellore Institute of Technology,  
Vellore – 632 014.

Contact Numbers:

+91-9401408475

E-Mail: [shanidul.hoque@@vit.ac.in](mailto:shanidul.hoque@@vit.ac.in)

+91-9973979622

E-Mail: [dilipkumar.choudhary@vit.ac.in](mailto:dilipkumar.choudhary@vit.ac.in)



## A One-day National Workshop (hands-on)

## Hands-on Workshop on RF & Microwave Circuits for Wireless Communication



Organized By

**Department of Communication Engineering**  
**School of Electronics Engineering,**

**VIT Vellore**