

About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

Course Content:

- Final Year Project Development
- Innovative ECE Project Selection
- Industry-Oriented Project Design
- IEEE Paper-Based Projects
- Hardware–Software Integration
- Project Documentation & Presentation
- Patent & Publication Guidance
- ECE Domain Exposure
- **Embedded Systems & IoT**
- **VLSI & FPGA**
- **AI/ML in ECE**
- **Signal & Image Processing**
- **Wireless Communication & 5G**
- **Antenna & RF Design**

Registration Fee:

UG/PG/Research Scholars// Faculty: **Rs. 200 +GST**
A certificate will be issued to all the registered participants.

Use following link to make online payment.

<https://events.vit.ac.in/>

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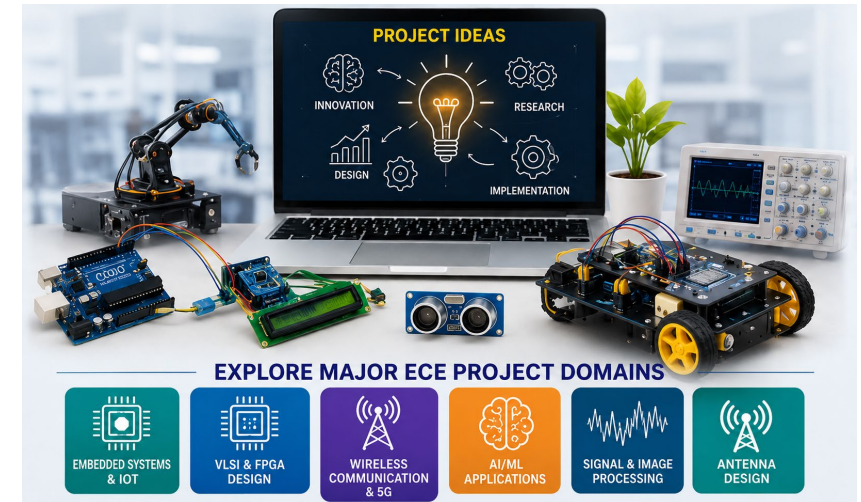
A One-day National Workshop

Exposure to Final Year Project Domains in Electronics and Communication Engineering From Idea Selection to Project Development

Date: 10th June 2026

Venue: Online

Time: 10.00 AM to 5.00 PM



Organized By

Department of Micro & Nanoelectronics
Department of Sensor and Biomedical Technology
School of Electronics Engineering, VIT Vellore