About SENSE:

SENSE at VIT was established for imparting state-of-the art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST

Course Content:

- Fundamentals of Digital Image Processing
- Machine Learning Techniques for Image Analysis
- Deep Learning for Computer Vision
- Recent Advances in AI-Based Image Processing
- Real-World Applications and Hands-on Implementation

Date: 13th June 2026
Venue: Online
Time: 10.00 AM to 5.00 PM

Registration Fee:

UG/PG/Research Scholars// Faculty: **Rs. 200 +GST**
A certificate will be issued to all the registered participants. Use following link to make online payment.

https://events.vit.ac.in/

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Dr. Jasmine Pemeena Priyadarisini

Professor and Dean,

School of Electronics Engineering (SENSE), Vellore Institute of Technology, Vellore, India.

Dr. Sundar S

Associate Professor & Head,

Department of Embedded Technology

SENSE, VIT Vellore

Dr. Kannadassan D

Professor & Head,

Department of Communication

SENSE, VIT Vellore

Coordinators

Dr. Bijaylaxmi Das
Dr. Shanidul Hoque

School of Electronics Engineering (SENSE), Vellore Institute of Technology, Vellore – 632 014.

Contact Numbers:
+91-8984764021
E-Mail: bijaylaxmi.das@vit.ac.in
+91-9401408475
E-Mail: shanidul.hoque@vit.ac.in

VIT
Vellore Institute of Technology
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A One-day National Workshop (hands-on)

Machine Learning for Image Processing: Fundamentals, Recent Advances, and Real-World Applications

Organized By

Department of Embedded Technology & Communication Engineering
School of Electronics Engineering, VIT