

About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST.

About the event:

This session will provide an overview of the key technologies and engineering challenges shaping the 6G ecosystem, and how modern Electronic Design Automation (EDA) and simulation methodologies can help address them. The discussion will cover system-level modelling and waveform development, RF and microwave circuit design, antenna and electromagnetic simulation, high-speed digital design, signal integrity, power integrity, and system-to-hardware validation.

Course Content:

- 6G Technology Landscape
- Engineering Challenges in 6G
- EDA for 6G Deployment
- From System Concept to Hardware
- Keysight Solutions for 6G
- Interactive Demonstration / Use Cases

Resource Person:

Keysight Technologies, Bangalore

Eligibility: The training program is open to Research Scholars and UG/PG students.

Registration Fee:

Rs.300 + 18% GST

Registration: <https://events.vit.ac.in/>

Advisory Committee:

Dr. Jasmine Pemeena Priyadarisini

Professor and Dean,
School of Electronics Engineering (SENSE),

Vellore Institute of Technology, Vellore,
India.

Dr. Kannadassan D

Professor & Head,
Department of Communication
Engineering, School of Electronics
Engineering (SENSE),
Vellore Institute of Technology, Vellore,
India.

Dr Vinoth Babu K

Professor & IAC Convener

Engineering, School of Electronics
Engineering (SENSE),
Vellore Institute of Technology, Vellore,
India.

Coordinators

Dr. Boopalan G

Dr Rajesh N

School of Electronics Engineering (SENSE),
Vellore Institute of Technology,
Vellore – 632 014.

Contact Numbers: 8124345622,
9976254449

E-Mail: boopalan@vit.ac.in
rajesh.natarajan@vit.ac.in

**Industry Academia
Conclave'26**



**Enabling 6G Innovation
through Advanced EDA
and System-Level
Design**

(Hybrid Mode)

8th September, 2026

Organized by

**Department of Communication
Engineering
School of Electronics Engineering
SENSE**