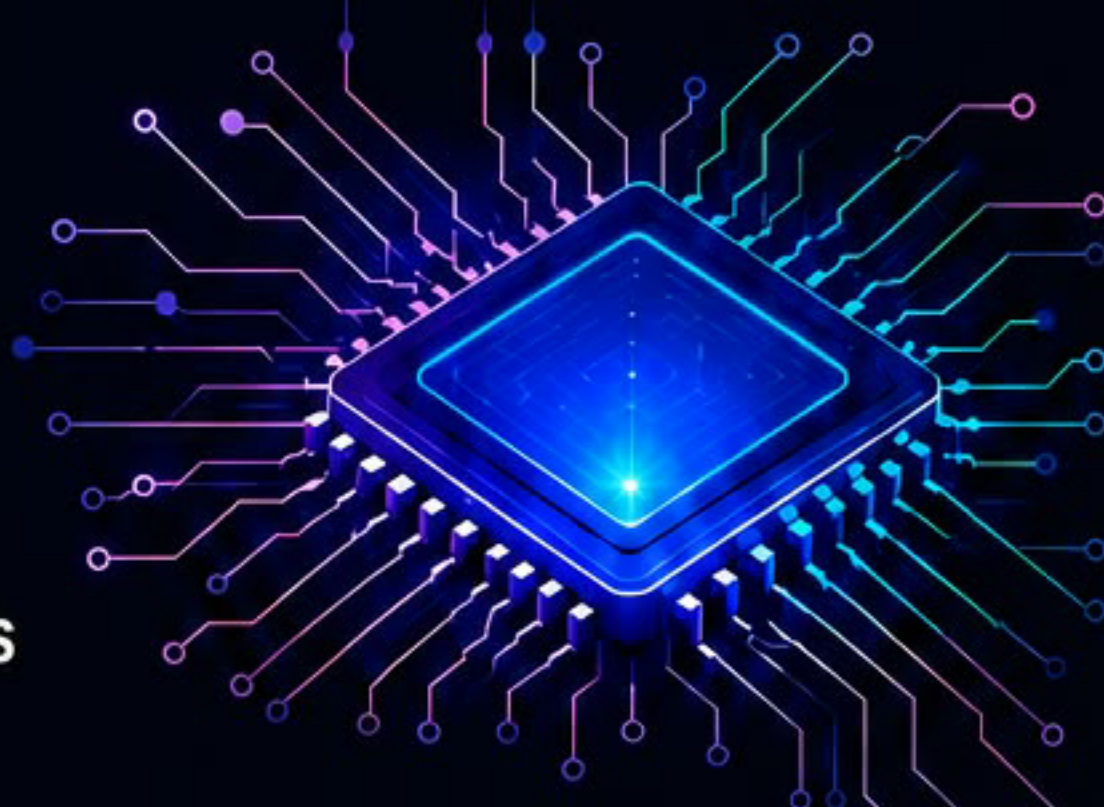




VIT
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

School of Electronics Engineering
Department of Micro and Nanoelectronics



INDUSTRY ACADEMIA CONCLAVE 2026

NextGen-Innovathon

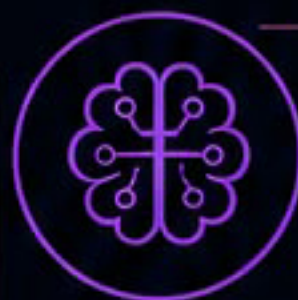
NextGen Innovation Challenge –
Design, Verify, Connect, Compute and Sustain

HACKATHON TRACKS



1 DESIGN & VERIFICATION

- Heterogeneous SoC Architectures
- Formal Verification & Assertions
- EDA Automation



2 EMERGING TECH & AI HARDWARE

- FPGA Accelerated AI
- Quantum Inspired Circuits
- Neuromorphic VLSI for Real-Time Edge Intelligence



3 IOT, SECURITY & CONNECTIVITY

- Secure Silicon
- Intelligent Sensor Hub for Wearable and IoT Applications
- Next Gen Interconnects



4 SUSTAINABILITY & LOW POWER DESIGN

- Low Power VLSI
- Green Silicon: Energy-Efficient Hardware for Sustainable Computing
- Dynamic Power Optimization for Edge SoCs



TEAM SIZE
3 Members



REGISTRATION FEE
500 + 18% GST



DATE
7th and 8th
Sep 2026



TOTAL PRIZE WORTH
₹ 30,000



TARGET PARTICIPANTS
UG & PG Students



COORDINATORS
Dr. Prayline Rajabai C
Dr. Aarthy M



CONTACT DETAILS
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aarthy.m@vit.ac.in



REGISTRATION LINK <https://events.vit.ac.in/>
Last date for registration: 3rd Sep 2026

BUILD THE NEXT GENERATION OF TOMORROW!