



VIT

Vellore Institute of Technology
(Deemed to be University under section 3 of
UGC Act, 1990)

Week 1 (September 15,16,17,18,19,20) - 12 hours

- Introduction to EMI & EMC
- Electromagnetic Fundamentals
- Noise Sources in Electronic Systems
- Coupling Mechanisms
- Signal Behaviour on PCB Traces
- Grounding Concepts
- EMC Standards & Regulatory Framework
- Conducted and Radiated Emission Standards
- Immunity Standards

Week 2 (September 21,22,23,24,25,26) - 12 hours

- Decoupling & Supply Stabilization
- Power Distribution Behaviour in PCBs
- PCB Layout for EMI Control
- Crosstalk & Signal Integrity Effects
- Shielding Techniques
- Filtering Techniques
- Cable & Connector Practices
- EMI in Consumer Electronics
- Automotive & Industrial EMC
- Medical & Aerospace EMC

Week 3 (October 5,6,7,8) - 8 hours

- Measurement Fundamentals
- Conducted and Radiated Emission Measurement Setup and testing
- Immunity Testing Setup
- Lab Familiarization
- EMI Reduction Experiment



REGISTRATION

<https://events.vit.ac.in/>



CONTACT



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CO-ORDINATORS

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ADVISORY COMMITTEE

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VAC2338 -

ELECTROMAGNETIC INTERFERENCE AND COMPATIBILITY (EMI/EMC) - BASICS AND STANDARDS



15th September to 8th October - 2026



Online - 15th September to 8th October
(6:00PM-8:00 PM)

Organized by

School of Electronics Engineering (SENSE)

Vellore Institute of Technology, Vellore



ELIGIBILITY

The Seminar is open to
Research Scholars and UG/PG
students.



REGISTRATION FEE

Rs. 300 + GST



RESOURCE PERSON

Dr. K. G. Sujanth Ph.D
Assistant Professor III,
SASTRA University,
Thanjavur.

SDG 3: GOOD HEALTH AND WELL-BEING
SDG 9: INDUSTRY, INNOVATION, AND INFRASTRUCTURE



SENSE

SCHOOL OF ELECTRONICS ENGINEERING
VIT, VELLORE

