

About SENSE:

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Faculties are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), and DST

Course Content:

- The Wireless Future – Introduction to 5G/6G and IoT
- Printed and Waveguide Antennas Design using Industry Standards software
- Fabrication Using Mipex PCB Prototyping Machine
- Characterization Using Vector Network Analyzer
- Practical Deployment Challenges in 5G/6G and IoT
- Hands-on Mini Project: Design and Testing of 5G/6G and IoT

Date: 17th October 2026

Venue: TT 145 (Hybrid)

Time: 10.00 AM to 5.00 PM

Registration Fee:

UG/PG/Research Scholars// Faculty: **Rs. 200 +GST**
A certificate will be issued to all the registered participants. Use following link to make online payment.

<https://events.vit.ac.in/>

Advisory Committee:

Dr. Jasmine Pemeena Priyadarisini

Professor and Dean,
School of Electronics Engineering
(SENSE), Vellore Institute of Technology,
Vellore, India.

Dr. Kannadassan D

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A One-day National Workshop (hands-on)

From Design to Deployment: Hands-On Printed and Waveguide Antennas for 5G/6G-Enabled IoT



Organized By

Department of
Communication Engineering
School of Electronics
Engineering, VIT